This index covers all technical items that appeared in this periodical during 1983, and items from prior years that were commented upon or corrected in 1983. The index is divided into an Author Index and a Subject Index, both arranged alphabetically.

The Author Index contains the primary entry for each item; this entry is listed under the name of the first author and includes coauthor names, title, location of the item, and notice of corrections and comments if any. Cross-references are given from each coauthor name to the name of the corresponding first author. The location of the item is specified by the journal name (abbreviated), year, month, and inclusive pages.

The Subject Index contains several entries for each item, each consisting of a subject heading, modifying phrase(s), first author's name, and enough information to locate the item. For coauthors, title, comments, and corrections if any, etc., it is necessary to refer to the primary entry in the Author Index.

### AUTHOR INDEX

**A**

Abadir, M. S., and H. K. Reghhabi. LSI testing techniques; *M-M Feb 83* 34–51

Abrahamson, David M. A fast entry path into user microcode on the VAX-11/780; *M-M Dec 83* 40–43

Adams, W. Thomas, and John Brady. Magnitude approximations for microprocessor implementation; *M-M Oct 83* 27–31

Attikouzel, John A., see Marshall, Trevor G.; *M-M Dec 83* 17–23


B

Brady, John, see Adams, W. Thomas, *M-M Oct 83* 27–31

C

Cawthron, Duane, see Huntsman, Clayton, *M-M Dec 83* 44–54

Cetera, Margaret M., see Lunney, David, *M-M Aug 83* 19–31

Childress, Dudley S., see Heckathorne, Craig W.; *M-M Jun 83* 17–23

Crabtree, David, see Nelson, Peter J.; *M-M Aug 83* 7–17

D

De Prycker, Martin. A performance comparison of three contemporary 16-bit microprocessors; *M-M Apr 83* 26–37

Dennis, Philip C. A heuristic routing algorithm; *M-M Apr 83* 48–54

F

Fathi, Eli T., and Moshe Krieger. An executive for task-driven multimicrocomputer systems; *M-M Oct 83* 32–41

G

Grossner, Clifford P., T. Radhakrishnan, and Andy Pospiech. An integrated workstation for the visually handicapped; *M-M Jun 83* 8–16

Gupta, Amar, and Hoo-min Doong. An architectural comparison of 32-bit microprocessors; *M-M Feb 83* 9–22

Gustavson, David B., and John Theus. Wire-OR logic on transmission lines; *M-M Jun 83* 51–55

H

Hartness, Richard V., see Lunney, David, *M-M Aug 83* 19–31

Heckathorne, Craig W., and Dudley S. Childress. Applying anticipatory text selection in a writing aid for people with severe motor impairment; *M-M Jun 83* 17–23


Huntsman, Clayton, and Duane Cawthron. The MC68881 floating-point coprocessor; *M-M Dec 83* 44–54

I


Jaeger, Richard C. Tutorial: Analog data acquisition technology: IV—System design, analysis, and performance; *M-M Feb 83* 52–60


K


Katsura, Koyo, see Maejima, Hideo; *M-M Dec 83* 9–16

Kihara, Toshimasa, see Maejima, Hideo; *M-M Dec 83* 9–16

Kirmann, Hubert. Data format and bus compatibility in multiprocessors; *M-M Aug 83* 32–47

Korba, Larry, see Nelson, Peter J.; *M-M Aug 83* 17–17

Krieger, Moshe, see Fathi, Eli T.; *M-M Oct 83* 32–41

L


† Check author entry for coauthors

‡ Check author entry for subsequent corrections/comments