A number of editorial board changes have occurred since the beginning of the year. Andrew Allison and Don Feinberg have resigned from the board, and Richard Markowitz has completed his term. We thank them for their help in guiding the magazine through its first year and a half of publication.

Robert Stewart has joined the board as associate editor for standards. Bob, from National Semiconductor Corporation, has been very active in standards work and is largely responsible for the existence of *IEEE Micro*. We are honored to have Kenji Kani of Nippon Electric Company join us as our first associate editor from Japan. Patrick Buffet, from Digital Equipment Corporation, replaces Don Feinberg on the board, and John Burkitt joins the editorial board as our first associate editor from Australia. Biographical sketches of these new associate editors appear at right.

Richard C. Jaeger, Editor-in-Chief
Peter R. Rony, Associate Editor-in-Chief

Robert G. Stewart is engaged in work on short-channel MOS device modeling at National Semiconductor Corporation. Presently a member of the IEEE Computer Society's Governing Board, he served as chairman of the society's Computer Standards Committee for 3 1/2 years. He initiated and had published in *Computer* magazine proposed IEEE standards for assembly language, the S-100 bus, floating-point arithmetic, Pascal, and the Multibus. He also initiated work on standards for extensions to high-level languages for microprocessors, the microprocessor operating system interface, local networks, and the Future Bus. He was instrumental in starting *IEEE Micro*.

A senior member of the IEEE, Stewart holds a PhD from the Illinois Institute of Technology, Chicago.

Kenji Kani joined Nippon Electric Co., Ltd., in 1965 and is now engineering manager of the Microcomputer Systems Department, IC Division. He was a lecturer at the University of Osaka from 1975 to 1977. Engaged in semiconductor device development, device modeling, computer-aided IC design system development, and microprocessor architecture, Kani received the 1970 Yonezawa Prize from the Institute of Electronics and Communication Engineers of Japan.

A member of the IECE of Japan and of the Information Processing Society of Japan, Kani received the BE, ME, and PhD degrees in electrical engineering from Waseda University in 1963, 1965, and 1971, respectively.

Patrick Buffet is microprocessor applications manager for Digital Equipment Corporation in Maynard, Massachusetts. His interests range from chips to development tools and performance. With the company since 1975, he has supervised the design of gate arrays and controller chips. Prior to joining DEC he was with Texas Instruments for eleven years, where he contributed managerially and technically to the original Schottky TTL line, the first one-transistor dynamic RAM, and a high-speed minicomputer LSI chip set.

Buffet has served for five years on the program committee of the International Solid-State Circuits Conference. His publications include tutorials on semiconductor devices and MOS digital design. He graduated from the Institut Polytechnique de Grenoble, France, in electrical engineering in 1962.

John Burkitt is editor of computer publications in the Computer Services Unit of the University of New South Wales. He served as editor of the *Control Systems Journal* (Australia) from 1967 to 1974. A member of the IEEE, he holds a BS in physical chemistry from the University of Sydney.