Introducing the IEEE Micro Editorial Board

The thirteen individuals we introduce here assist the senior editors in the solicitation and review of our articles and help define our editorial approach. Each brings to IEEE Micro extensive experience in his fields of specialty. Two members of the board have responsibility for specific departments—Andrew Allison coordinates the MicroStandards department, and Victor P. Nelson selects the items which appear in New Products and the Product Summary. In addition, Associate Senior Editor Peter R. Rony writes the Access department.

Richard C. Jaeger, Senior Editor

Andrew Allison, editor of MicroStandards in IEEE Micro, is an independent consultant to management in the field of mini/microcomputer technology assessment and market analysis. Previously, he spent over six years in various engineering and marketing positions with Digital Equipment Corporation and Rolm Corporation, and two years as the MOS microprocessor marketing manager at Advanced Micro Devices, Incorporated.

Allison holds a Diploma in Electrical Engineering (a United Kingdom equivalent of the BSEE), and is a member of the IEEE and the IEEE (senior member). He has been active in the Computer Society's Microprocessor Standards Committee since 1977 and has chaired the P896 (Advanced Microcomputer System Backplane Bus) working group since its inception in mid-1979.

Tuvia Apelweicz joined the Department of Electrical Engineering and the Communication Laboratory of the City College of New York in 1979. His research interests include the application of computer architecture to speech processing, voice encryption techniques, and packet switch networks. He has published several papers and is currently working on a book.

Previously, Apelweicz was a member of the technical staff of Bell Laboratories' data communication department, where he participated in the design of a high-speed, full duplex, two-wire modem for the direct-distance-dialing telephone network. Apelweicz received the PhD degree in electrical engineering from the City University of New York in 1978. He is a member of Eta Kappa Nu, Tau Beta Pi, and the IEEE.

James H. Aylor is an assistant professor of electrical engineering and director of the Pattern Analysis and Computer Systems Laboratory at the University of Virginia. Before joining the faculty in 1978, he was a research engineer with the university's Research Laboratories of the Engineering Sciences. His research interests are in the areas of digital system design, microcomputer applications, image processing, and rehabilitation engineering.

Aylor received the BS, MS, and PhD degrees in electrical engineering from the University of Virginia, Charlottesville, in 1968, 1971, and 1977. He is a member of Sigma Xi, Tau Beta Pi, andEta Kappa Nu, and he is chairman of the IEEE Computer Society's Technical Committee on Computing and the Handicapped.

Walter R. Beam is deputy for advanced technology in the Office of the Assistant Secretary of the Air Force. During a career spanning more than 30 years in academia and industry, Beam has worked in electron optics, microwave technology, and high-speed computer technology. He was a professor of electrical engineering at Rensselaer Polytechnic Institute from 1959 to 1964, and organized Rensselaer's Thin Films Laboratory. Later, he was director of engineering technology at IBM's Systems Development Division, supervising planning for the System/370. In 1970 he became a private consultant in the development and application of computer systems.

An IEEE Fellow, Beam is also a member of ACM. He received the PhD in 1953 from the University of Maryland.
J. T. Cain is an associate professor of electrical engineering at the University of Pittsburgh. His Computer Society activities include chairing the Mini/Microcomputer Technical Committee, membership and test ship in the Education Committee, and membership in the Computer Standards Committee. Cain has worked for Westinghouse Electric Corporation and Bell Telephone Laboratories. His research interest is in the applications of computing in real-time systems, particularly power systems.

Cain received the PhD in electrical engineering in 1970 from the University of Pittsburgh. He is a recipient of the ASEE Dow Outstanding Young Faculty Award, the Computer Society Special Group Award, and the ASEE Western Electric Fund Award for Excellence in Engineering Education.

Patrick P. Fasang is a member of the technical staff at Siemens Corporation Corporate Research and Technology Center in Cherry Hill, New Jersey. Previous affiliations include Sprague Electric Company, University of Portland, Bonneville Power Administration, and RCA. He has served as chairman of the IEEE Electronics Group, chairman of the IEEE Computer Society's Oregon chapter, and secretary of the Computer Society Education Committee.

Fasang received a BS from California State University at Fresno, an MS from California State University at San Jose, and a PhD from Oregon State University, all in electrical engineering. He is a member of Eta Kappa Nu, a senior member of IEEE, and an associate editor of IEEE Transactions on Industrial Electronics and Control Instrumentation.

John L. Hennessy has been an assistant professor of electrical engineering in the Computer Systems Laboratory, Stanford University, since 1977. His main research interests are programming language and compiler design, VLSI design aids, and computer architecture.

He is the designer of SLIM, a system for synthesizing PLA-based control for VLSI systems. Lately he has been directing the MIPS project, a high-speed VLSI microprocessor.

Hennessy received the BS degree in electrical engineering from Villanova University in 1973; he received the MS in 1976 and the PhD in 1977, both in computer science, from the State University of New York at Stony Brook.

Fred Liguori is head of the ATE (automated testing equipment) branch at the Naval Air Engineering Center, Lakehurst, New Jersey. Previously he was manager of the Software Systems Engineering Laboratory of the Emerson Electric Company. He has over 20 years' experience in electronic testing, and he has taught courses in ATE since 1966.

Liguori chairs the Instrumentation and Measurement Society's Technical Committee on Automated Instrumentation. He has served on numerous technical conference committees, including the Cherry Hill Test Conference and the ATE Seminar and Exhibit. He is author of Automatic Test Equipment: Hardware, Software, and Management.

Liguori is a graduate of Tufts University Electrical Engineering College and holds a Master's degree from Hofstra University.

Richard J. Markowitz is strategic product line manager for iAPX 286 microprocessors at Intel Corporation. His primary technical interests are microprocessor and systems architecture, operating systems, and new applications of microprocessors.

Previously, he was manager of component architecture and senior design engineer at Intel. Earlier experience at the T.R. Group, Burroughs Corporation, and Trans-World Airlines included the development of real-time language processors and operating systems.

Markowitz, the author of several articles about microprocessors, is a member of IEEE and ACM. He received a BS in physics from the Massachusetts Institute of Technology in 1963; did graduate work in computer science at the University of California, and received an MBA from Columbia University in 1976.

D. L. Feinberg has worked in the Telecommunications Industry Group of Digital Equipment Corporation, Merimack, New Hampshire, since 1977. He is responsible for micro and small system support and micro-based new product development. From 1973 to 1977, he was with Spacenet, Inc., Bedford, Massachusetts, where he worked on uniprocessor and multiprocessor telemetry ground stations and test hardware. At Sanders Associates, Nashua, New Hampshire, from 1970 to 1972, he was responsible for operating systems for mini-based automatic test equipment. Feinberg has published several papers and is preparing a book on technology trends.

Feinberg received the BS in physics from Michigan State University in 1968 and the MS in physics from Cleveland State University in 1972. He is a member of the IEEE.

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Victor P. Nelson, New Products editor for IEEE Micro, has been an assistant professor of electrical engineering at Auburn University since 1978. His main research interests are microprocessor applications, multiprocessor systems, and fault-tolerant computing. From 1971 to 1975, Nelson was an instructor at the US Naval Nuclear Power School, and from 1976 to 1978, a research assistant in the Digital Systems Laboratory at the Ohio State University.

Nelson received the BSEE from the University of Kentucky in 1971, and the MS and PhD from the Ohio State University in 1977 and 1978. A member of the IEEE Computer and IECI Societies, the ACM, Eta Kappa Nu, and Pi Mu Epsilon, Nelson is vice-chairman for fault tolerance of the Distributed Processing Technical Committee.

J. D. Nicoud is professor at the Swiss Federal Institute of Technology of Lausanne, Switzerland, in the Micro and Microprocessor Laboratory (Microinformatica). Since 1974 he has designed several microprocessor-based systems.

Nicoud's present interests include local networks and graphics work stations. He is active in the IEEE P896 and P694 working groups, and promotes electronic mail for faster communication between European and US researchers.

Deene Ogden is an engineer manager and senior member of the technical staff in the Digital Systems Group of Texas Instruments, Inc., in Austin, Texas. Since joining TI in 1971, he has been involved in minicomputer and microprocessor design and development. He was the principal architect of the TMS 9900 microprocessor system and has held positions in engineering, marketing, and strategic product planning. Prior to 1971, he worked in digital integrated circuit design for Westinghouse. Ogden's areas of technical interest include microprocessor and computer systems and semiconductor technology.

Ogden received a PhD in electrical engineering from the University of Missouri in 1971. He is a member of Phi Eta Sigma, Tau Beta Pi, Eta Kappa Nu, and the IEEE.