Mention Minnesota winters to out-of-staters and they'll usually associate with snow drifts, cold temperatures and a variety of ice-sculpted images. Granted, our weather traditionally dominates conversation this time of year, but around the Twin Cities, Sperry Univac Semiconductor employees talk up the positives.

Consider these activities: cross country skiing, ice hockey, snowmobiling, ice fishing, and even sleigh riding.

Yes, winter is an invigorating time here. But some feel that gathering around a crackling fireplace with friends and family while enjoying tasty refreshments and warm companionship is perhaps the best activity of all. We couldn't agree more.

**HIGH PRODUCTIVITY:**

Energy levels peak during this time of year. It's a good time for research, planning and concept development. Distractions are minimal. Major projects are completed, at the office and in the home. What better time to delve into advanced LSI and VLSI technologies?

**THE COMFORT FACTOR:** Our two major downtown business/shopping centers, Minneapolis and St. Paul, maintain glassed in "skyway systems" which interconnect literally whole downtown areas. Meaning, you rarely have to step outside.

**SNOW AND REMOVAL:** Although traditiona, Minnesota receives substantial snowfall every year, the Twin Cities averages proportionately less than elsewhere in the state. However, we have the necessary equipment and financial resources to keep our highways and neighborhood streets open and passable. Only the city of Montreal, Canada, spends more.

**TAKING ACTION:** The realities of relocation, especially to 4-season country, during cold weather cause some to pause. But why wait. Construction of our new 235,000 sq. ft. semiconductor manufacturing facility will increase Sperry Univac's total capabilities. And for skilled engineers and technicians: "the challenge is here and now."

**SEMICONDUCTOR OPPORTUNITIES:**

**MOS CIRCUIT DESIGN ENGINEER**

2 year minimum CMOS or NMOS circuit design experience. VLSI - complexity with 2 micron channel lengths.

**TEST** — BS degree plus 3 to 5 years experience with Sentry Test Systems.

**TESTABILITY** — MS or PhD in EE or Computer Science plus 3 to 5 years experience in testability area.

**MOS DEVELOPMENT** — CAD Engineer with MS or PhD in EE or Computer Science plus 3 to 5 years semiconductor CAD experience.

**WAFFER FAB SUPERVISOR** — Experience in either Bipolar or MOS fabrication processes for our expansion into second shift.

**FACILITIES/EQUIPMENT ENGINEER** — Experience in semiconductor facility construction, plant engineering, or equipment evaluation and maintenance desired.

**BIPOLAR PRODUCT ENGINEER** — 3 or more years experience in ECL TTL or analog. Requires BS or MSEE degree.

**BIPOLAR DEVELOPMENT ENGINEERS** — BS/MS or PhD with experience in Device Modeling and Characterization. Wafer Processing using state-of-the-art equipment and techniques, product and circuit design to work with high speed ECL Schottky TTL Gate Arrays, and custom high performance analog VLSI circuits.

**MANAGER MOS CIRCUIT DESIGN** — BSEE or MSEE degree plus strong background in MOS design. Will supervise the development of MOS design methodologies, as well as modeling techniques for both DC and dynamic parameters at transistor, circuit, and logic design levels. Involves design, layout and characterization of evaluation devices and includes specification and evaluation of automated design tools.

**THE BOTTOM LINE:**

We've touched on some opportunities well worth moving for. But there's more. Like high-quality, affordable housing. Get the details by calling Bill Dahlen Collect at (612) 456-2894. Or send a resume to his attention at: SPERRY UNIVAC SEMICONDUCTOR DIVISION (U2) 26 P.O. Box 3525, St. Paul, MN 55165.

**SPERRY+UNIVAC**

SEMICONDUCTOR DIVISION

"THE CHALLENGE IS HERE AND NOW."

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