Events sponsored or cosponsored by the Computer Society of the IEEE are so indicated by the society's logo. To list an event in Calendar, submit information at least six weeks before the month of the issue it will be published in. This magazine is published in February, April, June, August, October, and December. Send the listings to IEEE Design & Test, Calendar, 10662 Los Vaqueros Circle, Los Alamitos, CA 90720.

January 1988


ATE & Instrumentation Conf. West, January 11-14, Anaheim, California. Contact C. Chesnut, MG Expo Group, 1050 Commonwealth Ave., Boston, MA 02215; (800) 223-7126.


Workshop on High-Level Synthesis, January 24-27, Orcas Island, Washington. Contact E. Detjens, 1820 Carleton St., Berkeley, CA 94720; (415) 642-8248.

February 1988

Modeling and Simulation on Microcomputers, February 3-5, San Diego. Contact Soc. for Computer Simulation, PO 17900, San Diego, CA 92117; (619) 277-3888.


March 1988

Workshop on Fault-Tolerance in VLSI Structures, March 4, Durham, New Hampshire. Contact A. Rucinski, ECE Dept. of New Hampshire, Durham, NH 03824; (603) 862-1381.


Int'l Seminar on Digital Communications, March 8-11, Zurich. Contact P. Gunzburger, Hasler AG, TDS, Belpstrasse 23, CH-3000 Bern 14, Switzerland; 41-31-632808.

Simulation Symp., March 16-18, Tampa, Florida. Contact A. Jones, CS Dept., Florida Atlantic Univ., Boca Raton, FL 33431; (305) 393-3675.


April 1988


11th Workshop on Design for Testability, April 19-22, Vail, Colorado. Applications for attendance must be received by December 31, and all attendees are expected to give a short presentation. Contact T. Williams, IBM Corp., PO 1900, Dept. 67A/021, Boulder, CO 80301; (303) 924-7692.

May 1988

Pittsburgh Conf. on Modeling and Simulation, May 5-6, Pittsburgh. Contact W. Vogt, or M. Mickel, Modeling and Simulation Conf., 348 Benedum Eng. Hall, Univ. of Pittsburgh, Pittsburgh, PA 15261; (412) 624-9800.


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Work on a test vector language

Al Lowenstein, PCA

Two IEEE standards subcommittees met during the International Test Conference in Washington, DC, last September to share information about the VHSIC (very high speed integrated circuit) Hardware Description Language and to solicit participation in the development of a test vector language.

VHDL, which is likely to be formally approved by the end of 1987, is a hardware description language that supports the design, production, and maintenance of electronic hardware throughout its life cycle. It was jointly developed by the Standards Coordinating Committee (SCC20)’s Automatic Test Program Generation Subcommittee and the Computer Society’s Design Automation Standards Subcommittee’s VHDL Analysis and Standardization Group.

SCC20’s ATPG Subcommittee is now involved with DASS’s Test Working Group in the development of a test vector language standard. The effort is part of an overall project to generate the ATPG Output Format, a standard for the interchange of test vector, fault dictionary, and guided probe data.

A draft test vector language, created as part of the Tester Independent Support Software System program, has been submitted for review, and the committees are now deciding whether to use it in whole or in part. Work on other parts of the ATPG Output Format has not begun.

For more information on the test vector language effort, or on how to get involved, contact Al Lowenstein, PCA, 1800 Nort hern Blvd., Roslyn, NY 11576; (516) 484-4610. For more information on VHDL, contact Ron Waxman, University of Virginia, School of Eng. and Applied Science, Thornton Hall, Charlottesville, VA 22901; (804) 924-6086.

December 1987
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Int'l Symp. on Multiple-Valued Logic, May 24-26, Madrid. Contact E. Trillas, Investigaciones Cientificas, Serrano 117, 28006 Madrid, Spain; (34-91) 6216624.


June 1988


Int'l Symp. on Circuits and Systems, June 7-9, Espoo, Finland. Contact P. Heinonen, Tampere Univ. of Technology, Computer Systems Lab., PO 527, SF-33101 Tampere, Finland or O. Simula, Helsinki Univ. of Technology, Dept. of Technical Physics, SF-02150 Espoo 15, Finland.


Int'l Symp. on Fault-Tolerant Computing, June 27-30, Tokyo. Contact Y. Komamiya, 2-4-8 Kikuna, Kohoku Ku, Yokohama 222, Japan; 81-044-911811.

Int'l Symp. on Electromagnetic Compatibility, June 28-30, Wroclaw, Poland. Contact W. Moron, EMC Symp. Box 2141, 51-645 Wroclaw 12, Poland.

July 1988


August 1988

Siggraph Conf. and Exhibit. on Computer Graphics and Interactive Techniques, August 1-5, Atlanta. Contact A. Newton, Univ. of Waterloo, Dept. of CS, Waterloo, Ontario, Canada N2L 3G1; (519) 888-4534.


CAD/CAM Technology Transfer to Latin America Conf., August 22-26, Mexico City. Contact Instituto de Investigaciones Electricas, Apdo. Postal 5-849, 11590 Mexico, D.F.

September 1988

Int'l Test Conf., September 12-14, Washington, DC. Contact D. Thomas, ITC 88, PO Box 264, Mt. Freedom, NJ 07970; (201) 895-5260.

European Assoc. for Computer Graphics Conf., September 12-16, Nice, France. Contact INRIA, Service des Relations Exterieures, Domaine de Volucy—BP 105, 78153 Le Chesnay Cedex, France; 33-1-39635501.

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