If you're not finding Gate Array, Advanced Schottky, and SMT faults, maybe the fault lies with your tester.

Introducing Teradyne's L210i. The only true VLSI in-circuit tester.

There are a lot of other in-circuit testers out there. But on VLSI boards, all they test is your patience.

Now there's the L210i VLSI In-Circuit Tester from Teradyne. It has over 3,000 bidirectional test pins. With digital, analog, and memory testing on every pin. And 10 MHz pattern rates.

Most importantly, the L210i delivers 95% or greater fault coverage on VLSI boards, in just 1–2 months. Because it's the first and only in-circuit tester that handles the three biggest problems in in-circuit testing today.

Test ASIC's ASAP.

One of the biggest problems for in-circuit testers today is ASIC's. If they detect ASIC faults at all, it's only because you've spent weeks and weeks programming them.

Not so with the L210i. It has a unique VLSI device tester architecture. So you can use patterns from device design and test databases. The L210i's translators convert patterns quickly.

Plus, the L210i stores and processes lengthy test data efficiently. So you save valuable time in testing gate arrays and other semi-custom devices.

You won't find that in any other in-circuit tester.
clusters. Memory arrays. Or hard-to-isolate devices.

With its MultiMode capability, you can easily partition the board into functional clusters, making the most of the L210i's functional test and diagnostic techniques.

That means you'll never be trapped by in-circuit testability problems again.


With the L210i, you won't be plagued by unreliable tests of your high performance logic. False clocking. Or shifting signal timing.

Because the L210i features short, low inductance fixture wiring. Superb driver electronics. And powerful debug tools. So everything you test gets tested repeatedly. Including high speed ECL and FAST.* Even Advanced Schottky and CMOS, with their tricky overdrive impedances.

The L210i fears no logic.

The only in-circuit tester with an escape hatch.

The L210i is the only in-circuit tester that's flexible enough to test today's SMT

No-fault insurance.

Teradyne's L200 family has set the standard for VLSI board testing in this decade, driving test quality up to bring costs down.

Now the L210i offers the first practical VLSI in-circuit test solution. It's the system you need to boost board yields at system test. And make your in-circuit test strategy successful.

If you can't afford a tester that misses VLSI faults, you'd better find out more about the L210i. Write Teradyne, 321 Harrison Avenue, Boston, MA 02118. Or call Daryl Layzer, L200 Product Group, 617/482-2700, ext. 2808.

*Trademark of Fairchild Semiconductor Corp.

The L210i is Teradyne's first board tester for VLSI in-circuit testing, and in-circuit tester budgets.

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