SECOND CALL FOR PAPERS

IEEE INTERNATIONAL CONFERENCE ON COMPUTER DESIGN:
VLSI IN COMPUTERS & PROCESSORS

Sponsored by:
IEEE Circuits and Systems Society
Computer Society of the IEEE
IEEE Electron Devices Society

In Cooperation with:

ICCD '87
Rye Town Hilton, Rye Brook, New York
October 5-October 8

The International Conference on Computer Design: VLSI in Computers will cover all aspects of the design and implementation of VLSI computer and processor systems. The multi-disciplinary nature of the conference is intended to emphasize the interactions between system design, logic circuit design, memory design, architecture, software, computer-aided design, testing, packaging design and VLSI technology, as they relate to computer and processor system realization.

Original papers are especially solicited in the following areas as they relate to VLSI computer and processor design:

**Architecture and Algorithms:** Super Computers, Array Processors, VLSI Processors, Computer Networks, Pipelined/Parallel Processors, Concurrent Architectures, Object-Oriented Methods, Optical Computing, Voice Recognition and Synthesis, Data Base Machines, System impact of operating systems and languages.

**Computer-Aided Design:** Logic/Circuit Simulation, Timing and Layout Analysis, CAD Hardware Accelerators, Graphics and Imaging, Integrated CAD Systems, Distributed Workstations, Formal Verification, Layout and Synthesis, Silicon Compilation, Multiprocessing and Parallel Processing for CAD.

**Design and Test:** Mixed Design Methodologies, Image/Signal processors, Design for Testability, Design for Self-Test, Testing and Testability Analysis, Fault Modelling, Highly Reliable Computers.

**VLSI and Technology:** Storage: Optical, Magnetic, Semiconductor, etc.; Wafer Scale Integration, Packaging, Environmental Factors, Chilled CMOS, GaAs.


Papers are also solicited which describe innovative features of new products, and which describe the overall integration of Technology, CAD, Design and Test and Architecture into the Computer Design Process.

**Instructions to Authors**

Prospective authors are invited to submit a 1000 word summary. To be considered for review, 1000 word summaries must include the following information: (a) clear description of the contribution and why it is important, (b) for original research submissions state what is novel about the contribution, (c) for review and tutorial submissions state the contribution to the multi-disciplinary mission of the conference.

Summaries are to be submitted in triplicate (3), along with the author names, addresses, affiliations, office and home telephone numbers, by no later than **February 16, 1987** to **Technical Program Chair:**

Dr. Prathima Agrawal
AT&T Bell Laboratories
600 Mountain Avenue
Room 3D-480
Murray Hill, N.J. 07974
Telephone: (201) 582-6943

**PLEASE SEND ME MORE INFORMATION ABOUT ICCD '87.**

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