CALL FOR PAPERS

The Seventeenth International Symposium on Fault-Tolerant Computing

JULY 6-8, 1987
SHERATON STATION SQUARE
PITTSBURGH, PENNSYLVANIA, USA

Sponsored by: Computer Society’s of the IEEE Technical Committee on Fault-Tolerant Computing

The Fault-Tolerant Computing Symposium has, since 1971, become the most important forum for discussion of the state-of-the-art in fault-tolerant computing. In 1987 the symposium will be held in Pittsburgh, PA, USA. Prof. John Shen, ECE Dept., Carnegie-Mellon University, Pittsburgh, PA 15213, Tel. (412) 268-3001 is the Symposium Chairman.

FTCS17 will address all aspects of specifying, designing, testing, diagnosing and evaluating dependable and fault tolerant computing systems and their components. Particular emphasis will be placed on papers relating to practical experience with real time systems, switching systems and transaction systems as well as the application of fault-tolerance to the design of safety critical systems.

Information for the Authors: Authors should submit 6 copies of papers before the submission deadline December 5, 1986, to the Program Co-Chairman: Dr. Flaviu Cristian, IBM Research K55/801, 650 Harry Road, San Jose, CA 95120-6099, USA, and Mr. Jack Goldberg, SRI Int’l, 333 Ravenswood Ave., Menlo Park, CA 94025. Papers in areas a, b, and f should be sent to Dr. Cristian, and papers in areas c, d, and e, to Mr. Goldberg.

Papers should be no longer than 5,000 words, should include a clear description of the problem being discussed, comparisons with extant work, and a section on major original contributions. The front page should include a contact author’s complete mailing address, telephone number and net address (if available), and should clearly indicate the paper’s word count and the area to which the paper is submitted. Submissions arriving late or departing significantly from these guidelines risk rejections without consideration of their merits.

Papers relating to the following areas are invited:

a) Design methods and basic algorithms for distributed fault-tolerant systems
b) Specification, design, testing, verification of reliable software
c) Specification, design, testing, verification, and diagnosis of reliable hardware
d) Fault-tolerant hardware systems architectures
e) Reliability, availability, safety modeling and measurements

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