THE CONFERENCE

The 1986 IEEE INTERNATIONAL CONFERENCE ON COMPUTER-AIDED DESIGN will be held November 10-13, 1986, in Santa Clara, CA. The conference concentrates on CAD for electronic circuit design, and features 1 day of tutorials and workshops, and 3 days of technical papers from an internationally recognized group of CAD experts.

TECHNICAL SESSIONS

The thirty-six technical sessions at ICCAD-86 fall into four general categories: systems, simulation, test, and layout. A partial list of topics is shown below.

- Gate Matrix Layout
- Global and Detailed Routing
- CAD Tool Integration
- Simulation on Multiprocessors
- Hardware Accelerators
- Module Generation
- Layout and Behavioral Synthesis
- Design for Testability
- CAD for Process Design
- Circuit Simulation
- Fault Simulation
- Global and Detailed Routing
- Timing Analysis
- Interconnection Modelling

The evening of Tuesday, Nov. 11, 1986, will feature 2 outstanding panel sessions.

- Is There a Future in Parallel Processing for CAD Applications?
- Which Operating System Will Win for CAD?

WORKSHOPS AND TUTORIALS

On Monday, Nov. 10, 1986, ICCAD-86 will sponsor five all-day workshops and tutorials:

- Silicon Compilation
- VLSI Layout
- EDIF Workshop
- Design-for-Testability
- Information Management for Engineering Design

HOTEL INFORMATION

A block of rooms has been reserved at the Doubletree and at the Santa Clara Marriott. The Doubletree is located beside and attached to the Santa Clara Convention Center, and the Marriott is about 4 blocks away.

For reservations, call: (800) 528-0444 (Doubletree) or (408) 988-1500 (Marriott). Be sure to mention ICCAD '86. Reservations received after October 26, 1986 will be subject to availability.

FOR FURTHER INFORMATION

Contact M.P. Associates, 7366 Old Mill Trail, Suite 101, Boulder, CO 80301 Telephone: (303) 530-4562