Choose LASAR logic simulation and win the board game.

RULES OF PLAY:
The Simulation Game is for design engineers and CAE/CAD/ATE managers of all ages. Any number can play.
The object is to uncover design flaws in devices, boards, and systems. Verify performance. Avoid multiple prototypes. And create quality tests.
The winner will get to market fastest, and earn great profits. Ready? Let's begin.

Visiting A Model Library.
First, move your game piece and look at each simulator's model library. But watch out! Don't risk everything on library size alone.
Instead, insist on quality of information as well as quantity. Look at LASAR Version 6.
It's also the only library with the Intel Bridge: complete device models directly from Intel's design data base. Its models of parts like the 8051, 8086, 80186, and 80286 are a real shortcut for new board designs.

What's not in the library, LASAR helps you build. With behavioral and register transfer languages to model from the top down. Structural and functional primitives to build from the ground up. Combine models at any hierarchical level for true mixed mode simulation.

So, do you choose libraries with sheer volume? Or with volumes of useful information?

Avoiding States Of Confusion.
Now answer a logical question. Is the simulator with the most logic states the most accurate?
LASAR Version 6 has 15 logic simulation values. Plus min and max signal strengths for every drive state, to help you navigate tricky electrical currents around buses, wired nets, and bidirectional transfer gates. All told, equivalent to more than one billion states.

With one picosecond resolution, LASAR also analyzes worst-case timing. It's the only simulator that reliably predicts hazards by eliminating common timing ambiguity.

With LASAR, you always know what state you're in, regardless of circuit design or IC technology.

State the obvious. Is LASAR's simulation accuracy best? Sorry, an incorrect answer here and you end up in the wrong state.

Escaping The Speed Trap.
Proceed with caution as you compare simulation speed.

Events per second is only one clue to performance. Evaluation accuracy is the other.

LASAR simulation adds full logic and min/max timing analysis, and detailed hazard reporting, for the most complete design evaluation possible. Without any speed penalty.

New LASAR SimuLite™ verifies circuit functions three to eight times faster than other logic simulators.

And concurrent time-based fault simulation with LASAR is five to 50 times faster than the competition.

Time again to choose. Then resume speed.

Bridging The Gap Between Design And Test.
LASAR Version 6 connects CAE/CAD with ATE.

Its netlist converters translate circuit schematic data from leading CAE and CAD systems. With UNIX and VMS versions, it runs on leading workstations and minicomputers.

And LASAR comes from Teradyne. We know ATE hardware. So patterns and diagnostics move from design to prototype and production test with ease.

Others say they bridge the gap. But don't get left hanging. You'll be on firm ground with LASAR.

Winning The Game.
Simulation is not a trivial pursuit. But with LASAR Version 6 you can win the board game. Gain a monopoly on good design. And get to market fast.

Don't play around.
Write Teradyne, 321 Harrison Avenue, Boston, MA 02118. Or call someone who knows the score: Daryl Layzer, LASAR Software Product Group, (617) 482-2700, ext. 2808.