IEEE DESIGN & TEST of Computers

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THEME FEATURES

Guest Editor's Introduction
Jerry Werner

IC Design Capability Conversion from Mainframe to Workstation Environment
Dave W. Palmer and John A. Wisniewski

How can we integrate engineering workstations into a real design environment?

Benchmarking Engineering Workstations
Mark A. Linton

What are the underlying performance issues of workstation hardware?

A Supercomputer Workstation for VLSI CAD
Rolf-Dieter Fiebrich

What role will general-purpose accelerators play in the workstation environment?

VEGA: A Visual Modeling Language for Digital Systems
Akira Sugimoto

What might next-generation user interfaces look like?

SPECIAL FEATURES

A Wirelist Compare Program for Verifying VLSI Layouts
K.L. Kodandapani and Edward J. McGrath

If we verify circuits as we lay them out, we discover errors up front when layout changes are more easily managed.

A New Economical Implementation for Scanable Flip-Flops in MOS
Dilip K. Bhavasar

Overall DFT cost performance depends upon how scanable flip-flops are implemented.

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