THEME FEATURES

The VHSCIC Hardware Description Language—A Glimpse of the Future

Guest Editor's Introduction Ron Waxman

VHDL Motivation

Allen Dewey and Anthony Gadient

VHDL’s motivation came from the Department of Defense, but the need is universal.

VHDL—Feature Description and Analysis

J.H. Aylor, R. Waxman, and C. Scarratt

Language development entails defining language requirements and analyzing language features.

VHDL—The Language

Roger Lipsett, Erich Marschner, and Moe Shahdad

From abstraction to application: a comprehensive view of VHDL’s organization and salient features.

VHDL—The Designer Environment

Alfred S. Gilman

Design and documentation: VHDL’s open-ended system integrates the existing toolset while accepting other tools as they become available.

VHDL’s Impact on Test

Al Lowenstein and Greg Winter

Hardware design and description subsume the ability to describe a unit under test: How is VHDL used within the test environment?

VHDL Critique

J.D. Nash and L.F. Saunders

The good, the deficient, and the controversial: Can VHDL be improved?

SPECIAL FEATURE

WRAPLE: The Weighted Repair Assistance Program Learning Extension

W.R. Simpson and C.S. Dowling

Before repairing systems, faults must first be isolated. WRAPLE’s fault isolation strategies, rather than deriving from estimates, incorporate fixed experience.

April 1986

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