PERCEPTION

"A rationally derived awareness discerned through the senses and prompted by direct or intuitive recognition" - the first step in the long, careful process that leads to scientific discovery. We consider our five Laboratories to represent the senses of GTE. As its central research facility, our intellectual curiosities reach out for new ideas and concepts, from which unique innovations and viable technologies may emerge and in turn be re-channeled throughout the Company's global operations.

Design Automation Research

For Design Automation Research, GTE's Computer Science Laboratory is a highly stimulating environment involving research and development projects in silicon compilers, design for testability, systems partitioning, and other design automation techniques. Joining a team of talented colleagues, you'll have the freedom to pursue new ideas to advance the future of GTE, as well as the future of technology.

Silicon Compilers Circuit Design

The Silicon Compiler Development Group is seeking a Circuit Designer familiar with CMOS technology to design and implement procedurally defined circuit elements for the circuit synthesis program. A BSEE is required; an MS is preferred. VLSI circuit design experience is desired. A working knowledge of LISP or other computer languages would be a plus.

Testing

We are looking for a leader to serve as principle investigator on our Automatic Test Generation team. Your responsibilities will include investigating fast ATG heuristics and physical fault models.

A position also exists in our Automatic Design for Testability project. In this position you will be involved with investigating and automating built-in self-test techniques and integrating them with the silicon compiler. PhD/MS in EE/CS plus IC design and testing experience preferred.

Systems Partitioning

The Systems Partitioning Project seeks a person able to define and develop new algorithms for dividing a systems functionality into smaller parts that fit onto physical units. The partitioning algorithm will satisfy a systems designer's tradeoff criteria of multiple objectives. The algorithm will be able to assist system designers at any level in the design hierarchy. Suitable candidate should have some knowledge of algorithm complexity. Experience in design of CAD tools and/or knowledge of hardware will be useful. Computer science, electrical engineering or mathematics background desired.

GTE Laboratories offers an attractive facility located in a quiet, wooded setting just outside of Boston, as well as a highly competitive salary and benefits package. Please send your resume in confidence to Cynthia Farrar, Human Resources Department, GTE Laboratories, Box DT885, 40 Sylvan Road, Waltham, MA 02254. An equal opportunity employer, M/F.