Designing VLSI Tests is a Waste of Time.

When you want to get your product on the market, there's no time to waste. But ensuring production quality requires testing, and generating tests is time-consuming. Unless you use AIDSTG.

100% VLSI Automatic Test Generation
AIDSTG is software that automatically generates test vectors for SCAN design LSI and VLSI chips and digital systems. It's fast and accurate. It shortens the design cycle, speeds your product to market, and ensures the highest quality product.

Design Audits
AIDSTG checks for compliance with the appropriate SCAN discipline and pinpoints areas of noncompliance. Automatically and thoroughly.

Fault Grading
AIDSTG provides the correct behavior pattern expected from a good chip. When discrepancies occur, it predicts where the failure might be. This diagnostic information eliminates guesswork and saves you time.
AIDSTG runs on DEC VAXs, IBM mainframes, and SUN workstations. Because it's fast, it requires minimum CPU resources. Generating test vectors for a typical 5,000 gate chip, for example, takes only one hour on a VAX 11/780.

Precision for All SCAN Design Logics
AIDSTG works with all SCAN design variations. So whether you use serial SCAN, parallel SCAN, or functional SCAN logic, AIDSTG can do the job.

It can generate tests for even the most complex systems—up to 100,000 gates. Or, if run on a large mainframe, it can handle up to 250,000 gates.
And because AIDSTG is precise and reliable, it's ideal for those who have to meet stringent military requirements. It's no wonder that major aerospace and defense contractors and semiconductor producers use AIDSTG.

The Most Complete Test Generation Tool
Test generation, design audits, and fault grading add up to the most complete VLSI testing tool available.
AIDSTG, the fast, efficient, and cost-effective way to ensure production quality. From Gateway Design Automation Corporation, the high-performance CAD company that knows you don't have time to waste.

GATEWAY DESIGN AUTOMATION CORPORATION
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