THE COMPETITION GAVE UP

When it came to really tough physical design problems the others couldn’t cut it. So they cut out.

Now, when companies like National Semiconductor, Motorola, Harris, Hughes and RCA—companies with challenging layout problems—need solutions, they come to the only firm that specializes in physical layout software tools, VR Systems.

Our MERLYN-G™ Automated Gate Array Layout Software handles chip designs up to 20,000 gates, and our MERLYN-PCB™ Automated Printed Circuit Design System designs 16-layer boards with up to 350 IC’s per board and 2.5 equivalent 14-pin DIPs per inch density—both on a variety of popular computers and workstations, such as DEC-VAX, Apollo, Tektronix 6000, IBM, and ELXSI; and with interfaces to other CAE/CAD/CAM functions and software. And our development of other layout technologies and tools continues.

For instance, TurnChip™ is the first “turnkey” layout product available that allows gate array logic designers to produce functionally equivalent physical designs to those of foundry design centers, without requiring mastery of physical layout issues.

VR is the layout technology leader. We have the largest engineering staff in the business, dedicated only to physical design technology for the electronic engineering market, and serious about solutions to your design problems.

In benchmark after benchmark, when the others couldn’t cut it, VR technology has come through. So, bring your layout problems, no matter how easy or tough, to VR Systems. VR is a part of the total design and test solution offered by Tektronix. For information about VR Systems’ products and/or the Design & Test Solution from Tektronix, call or write us, today.

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