Shortcut to success.

VLSI Design and Test

GenRad’s state-of-the-art engineering test products are meeting the crucial demands of VLSI design and test. HILO-2™ is the most powerful and efficient simulation tool available for validating designs against functional specifications. And, with the development of HITEST,™ we have pioneered the use of Artificial Intelligence (AI) technologies for developing test programs for large integrated circuits.

GenRad’s rapid growth environment offers you the excitement of a start-up without the hassles. Our decades of leadership in high technology give you the resources and stability of a large corporation, along with the challenge of start-up engineering. Join our team of advanced software development engineers as we create new approaches in logic verification and test generation systems.

Senior Software Engineers

Utilize your expertise in design languages or CAD to create our next generation of design verification and “design for test” tools. A background that includes CAE tool development experience with UNIX* or VMS and “C” is desirable. A knowledge of hardware description languages, design verification tools and automatic test pattern generation in a rule-based environment is desirable. A BSEE/CS/Math and two-plus years’ industry experience are required.

Join the excitement at GenRad. Call Jeannette Feldman at (408) 727-4400 for further details. We offer a full range of benefits including: stock purchase plan, cash profit sharing, tuition prepayment and relocation assistance. If you prefer, send your resume to GenRad, Inc., Professional Employment, DT2, 2855 Bowers Avenue, Santa Clara, CA 95051. We are an equal opportunity employer, m/f/h/v.

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