THEME: Pathways to Design Productivity

PURPOSE: To bring together the designers of microprocessor chip families and the designers of computer aided engineering (CAE) work stations so they can share their ideas and experiences regarding:

1. the impact of CAE on microprocessor design and
2. the impact of microprocessors on current and future CAE equipment and systems.

PROGRAM

Tuesday, April 2 - LUNCHEON ADDRESS: Dr. Paul Low, Vice-president, IBM
PLENARY SESSION: Jack Browne, Manager of Marketing & Applications, Motorola Inc.
Leif Rosqvist, Vice-president, Calma Co.

TECHNICAL SESSIONS
Wednesday, April 3 & Thursday, April 4

Session 1 - CAE Image Processing
- A Low Cost Image Processing System
- Using Vision Feedback In Printed-Circuit Board Assembly
- Image Processing/Computer Vision Work Station
- Simple Algorithms For Smoothing Noisy Character Sets
- A Programmable Graphics Processor With Reconfigurable Image Memory

Session 2 - Microprocessors & Simulation Modeling
- Memory Considerations In The Use of Microprocessors For Finite Element CAD Stations
- A High Performance Processor Utilizing Alternative Numeric Representation
- A Functional Mapping For Microprocessor System Simulation
- 32 Bit CMOS Microprogrammable Microprocessor

Session 3 - Languages/Data Bases
- Advanced Symbolic Debugger For C Language
- Database Management For VLSI Design
- Comprehensive Microdiagnostics Development For A VLSI Subsystem
- A Design Methodology Integrating Microprocessor Based Systems, Mainframe Computers, and Integrated Circuit Manufacturing

Session 4 - CAE Microprogramming & Modeling
- Functional Logic Modeling And Simulation on Personal Computers
- Microprogramming Design Alternatives
- System Design Of A Nodal Communications Processor
- Modeling of CAD/CAE Data

Session 5 - CAE Systems In The Building Industry
- The Computerization Of The ACE Industry
- Integrating CAD Into The BP Industry
- Market Forecasting: The Economics Of Scale
- Opportunities Of Microprocessors In The Building Industry
- A Knowledge Based Design System

Session 6 - Design With Work Stations
- An Integrated Semicustom Design System
- Genesil—A Real Silicon Compiler
- A Programmable Microprocessor Peripheral IC For Real Time Signal Generation And System Test Applications
- Workstation Architecture For Computer-Aided Design

Session 7 - Artificial Intelligence And Expert Systems
- Speakers/Panel
- Session 8 - Protection Of Proprietary Rights Speakers/Panel
- Session 9 - Engineering Work Stations For The Handicapped Speakers/Panel

EXHIBITORS - Preliminary listing
Calma Data General Tektronix
Daisy Autodesk Northwest
CDC Valid CAE Systems

1985 IEEE VLSI TEST WORKSHOP
Monday, April 1 & Tuesday morning, April 2
(to be held in conjunction with Forum)

NATIONAL MICROMOUSE CONTEST
Tuesday evening, April 2

For INFORMATION, call Ms. H. Yovan
Univ. of Pennsylvania, (215) 898-8134, 8106
Ms. H. Yovan, IEEE office, Moore School
Univ. of Pennsylvania, Philadelphia, PA 19101

Please send me information on the Microprocessor Forum and related activities.

Name: ____________________________
Address: __________________________
Phone: ____________________________