MATEO VALERO TO RECEIVE 2015 IEEE CS SEYMOUR CRAY COMPUTER ENGINEERING AWARD

In “recognition of seminal contributions to vector, out-of-order, multithreaded, and VLIW architectures,” Mateo Valero, a professor in the Computer Architecture Department at UPC in Barcelona, has been named the 2015 IEEE Computer Society Seymour Cray Computer Engineering Award recipient.

Valero is also director of the Barcelona Supercomputing Center at Spain’s National Center of Supercomputing. His research is in the area of computer architecture, with special emphasis on high-performance computing (HPC), including processor organization, memory hierarchy, numerical algorithms, compilers, and performance evaluation and runtime-aware architecture for multicore systems. He has been associate editor of IEEE Transactions on Parallel and Distributed Systems, IEEE Micro, IEEE Computer Architecture Letters, and Parallel Programming Languages, and the editor of several special issues of IEEE Transactions on Computers and Computer magazine.

The Seymour Cray Computer Engineering Award is one of the IEEE CS’s most prestigious awards and is presented in recognition of innovative contributions to HPC systems that best exemplify the creative spirit demonstrated by Seymour Cray. The award consists of a crystal memento, a certificate, and a US$10,000 honorarium.

Previous Seymour Cray Award recipients include Gordon Bell, Ken Batcher, John Cocke, Glen Culler, William J. Dally, Monty Denneau, Alan Gara, John L. Hennessy, Peter Kogge, Kenichi Miura, Steven L. Scott, Charles Seitz, Burton J. Smith, Marc Snir, Steven Wallach, and Tadashi Watanabe.

The 2015 IEEE CS Seymour Cray Computer Engineering Award will be presented at the SC15 Conference awards plenary session in Austin, Texas, on 17 November 2015 (http://sc15.supercomputing.org).

For more information about IEEE CS awards, visit www.computer.org/awards.

KATHERINE YELICK TO RECEIVE 2015 ACM/IEEE CS KEN KENNEDY AWARD

For her innovative research contributions to parallel computing languages used in both research and production environments, as well as her strategic leadership of the national laboratories’ development of novel educational and mentoring tools, Katherine Yelick, professor of electrical engineering and computer sciences at the University of California at Berkeley and faculty scientist at Lawrence Berkeley National Laboratory, will receive the 2015 ACM/IEEE Computer Society Ken Kennedy Award.

Yelick’s work has improved the programmability of HPC through innovations to parallel languages and runtime systems, and her contributions to compiler research and open source software were critical to the success of a new parallel programming model known as partitioned global address space (PGAS), an important software innovation for developers facing the challenges of exascale computing. She developed new automatic performance-tuning techniques and runtime systems that maximize performance across a wide variety of computer architectures.

ACM and the IEEE CS cosponsor the Ken Kennedy Award, which recognizes substantial contributions to programmability and productivity in computing and significant community service or mentoring contributions. It was named for the late Ken Kennedy, founder of Rice University’s computer science program and a world expert on HPC. The Ken Kennedy Award carries a US$5,000 honorarium endowed by ACM, the IEEE CS, and the SC Conference Steering Committee.

The award will be presented at SC15 on 17 November 2015 in Austin, TX.

IEEE COMPUTER SOCIETY INTRODUCES INTEL-SPONSORED PLATINUM AWARD PROGRAM

The IEEE Computer Society and Intel have joined together to establish a new competition for software developers. The Intel Platinum Professional Software Developer Award will recognize the individuals who achieve the three highest exam scores on the IEEE CS’s Professional Software Developer Certification before 30 May 2016. Each awardee will receive US$3,000 and be honored at the annual IEEE CS awards banquet in June 2016. The competition is open to software developers worldwide.

The IEEE CS Professional Software Developer Certification requires successful completion of two parts:

1. An online multiple-choice exam covering the following four key SWEBOK knowledge areas: Software Engineering Requirements, Software Engineering Design, Software Engineering Construction, and Software Engineering Testing; and

2. Successful completion of Applied Modules I and II on the exam, which requires the candidate to perform Java coding.

For more information on the IEEE CS/Intel award, visit www.computer.org/web/education/professional:software-developer-certification, and for more information on the IEEE CS Certification and Credential Program, visit www.computer.org/web/education/certifications.