CALL FOR ARTICLES FOR COMPUTER

Computer seeks articles for a February 2007 special issue on high-performance reconfigurable computing systems and applications. Guest editors are Duncan Buell from the University of South Carolina, Tarek El-Ghazawi from George Washington University, Kris Gaj from George Mason University, and Volodymyr Kindratenko from the University of Illinois at Urbana-Champaign.

Reconfigurable computing systems based on combinations of conventional processors and field-programmable gate arrays have been gaining attention in recent years. These synergistic designs have the potential to exploit coarse-grained functional parallelism through conventional parallel processing, as well as fine-grained parallelism through direct hardware execution on FPGAs.

A number of high-performance reconfigurable computing systems have been developed recently by major high-performance computer vendors such as Cray and Silicon Graphics and by emerging companies such as SRC and Starbridge. As these systems come into wider deployment, developers must address issues regarding their programmability and their usefulness to the high-performance computing community.

Computer invites papers that provide a perspective on all aspects of high-performance reconfigurable computing. Examples of suitable topics include hardware platforms, applications, programming paradigms, compilers, programming languages, standards, and performance prediction and measurement.

Computer also encourages submissions that provide a comprehensive review of the state of the art and future trends in FPGA technologies, modern high-performance production and experimental reconfigurable platforms, high-level programming languages, and experiences in developing and running engineering and scientific applications on reconfigurable platforms.

The deadline for papers is 1 August 2006. Detailed author guidelines and submission instructions are available at www.computer.org/portal/pages/computer/mc/author.html. Send inquiries to the guest editors at buell@sc.edu, tarek@gwu.edu, kgaj@gmu.edu, or kindr@ncsa.uiuc.edu.

OTHER CALLS


JULY 2006


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SEPTEMBER 2006

4-8 Sept: VL/HCC 2006, IEEE Symp. on Visual Languages & Human-Centric Computing, Brighton, UK. www.cmis.brighton.ac.uk/vlhcc


**IEEE International Workshop on Memory Technology, Design, and Testing**

The annual MTDT event draws experts working in all facets of memory technologies to discuss memory design, process technology, and testability-related topics that include memory circuit designs, cell structures, fabrication processes, and design architectures.

This year, the overarching conference theme is “Enabling Technologies for Memory Reliability and Yield.” Organizers have invited papers on related topics that include testing and verification methods for SRAM and DRAM, flash memory, embedded memories, logic-enhanced and FIFO memories, three-dimensional memories, and content addressable memories.

MTDT, set this year in Taipei on 2-4 August, is sponsored in part by the IEEE Computer Society Test Technology Technical Council.

For more information on MTDT 2006, visit http://larc.ee.nthu.edu.tw/~mtdt06/

**Submission Instructions**

The Call and Calendar section lists conferences, symposia, and workshops that the IEEE Computer Society sponsors or cooperates in presenting.

Visit www.computer.org/conferences for instructions on how to submit conference or call listings, as well as a more complete listing of upcoming computer-related conferences.