CALL FOR IEEE CS PUBLICATIONS

IEEE Transactions on Information Technology in Biomedicine will feature a special issue on mHealth: mobile technologies for health applications.

Potential topics include Web-based mobile medical systems; 2.5G/3G mobile systems and ad hoc networks; sensors and signal processing for mHealth applications; space telemedicine and satellite applications; and standardization, regulation, and legal issues. Guest editors are Robert S.H. Istepanian from Brunel University, Emil Jovanov from the University of Alabama in Huntsville, and Yuanting Zhang from the Chinese University of Hong Kong. The full call for papers is available at www.vtt.fi/tte/samba/projects/titb/. The submission deadline is 30 Aug.

IEEE Transactions on Information Technology in Biomedicine is now cosponsored by the IEEE Computer Society.

OTHER CALLS


ICDM 2003, 3rd IEEE Int'l Conf. on Data Mining, 19-22 Nov., Melbourne, Fla. Workshop proposals due 15 May, papers and tutorial proposals due 10 June, panel proposals due 30 June. www.cs.uvm.edu/~xwu/icdm-03.html


CALLER AND CALENDAR

JUNE 2003


1-4 June: Southwest Test Workshop, Long Beach, Calif. www.swtest.org


9-11 June: ISCA 2003, 30th Int'l Symp. on Computer Architecture, (with FCRC), San Diego, Calif. www.cs.nyu.edu/isca03/


Submission Instructions

The Call and Calendar section lists conferences, symposia, and workshops that the IEEE Computer Society sponsors or cooperates in presenting. Complete instructions for submitting conference or call listings are available at www.computer.org/conferences/submission.htm. A more complete listing of upcoming computer-related conferences is available at www.computer.org/conferences/.

May 2003


**JULY 2003**


7-8 July: VG 2003, 3rd Int'l Workshop on Volume Graphics, Tokyo. www.inv.is.ocha.ac.jp/~vg03


**AUGUST 2003**

4-5 Aug: MTDT 2003, IEEE Int'l Workshop on Memory, Design, & Testing, San Jose, Calif. Contact adsingh@eng.auburn.edu


**SEPTEMBER 2003**


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**Call for Articles for Computer**

*Computer* seeks articles for a December 2003 special issue on power-aware and temperature-aware computing. The guest editors are Mircea R. Stan and Kevin Skadron from the University of Virginia.

An integrated and coordinated set of design techniques is required to successfully manage growing power and thermal demands while still meeting performance, reliability, and cost targets.

Despite the continuous scaling of processes and supply voltages and the increased awareness of low-power issues in the computer design and design automation communities, power consumption by state-of-the-art integrated circuits has continually increased in recent years. This apparent paradox results from the increased density enabled by process scaling and a multilayer interconnect, coupled with increases in area, especially in clock frequency, and driven by an insatiable market for high-performance systems.

Temperature is also on the verge of becoming a first-class design constraint along with the more traditional cost, performance, and power constraints. The increasing importance of thermal effects stems from an exponential increase in on-chip power densities, with today’s processors having power densities greater than a hot plate and within an order of magnitude of a rocket nozzle.

Research, characterization, and survey papers are requested for this special issue. Topics of interest include circuit, architecture, and system techniques for regulating static and dynamic power; improved models for power, current delivery, and operating temperature; compiler-hardware and OS-hardware cooperation; and methods for optimizing energy efficiency subject to real-time or quality-of-service requirements.

Send inquiries to mircea@virginia.edu and skadron@virginia.edu. Submit pdf documents by 1 June to computer-ma@computer.org and specify the December 2003 special issue on power-aware and temperature-aware computing.

See http://computer.org/computer/author.htm for author guidelines.