Preliminary Program

RTSS-87
Eighth Real-Time Systems Symposium

December 1-3, 1987
Fairmont Hotel
San Jose, CA

Sponsors:
The Computer Society of the IEEE
TC on Data Engineering
TC Distributed Systems
TC Fault-Tolerant Computing
TC Operating Systems
TC Software Engineering

Scope: This symposium addresses all aspects in the design, development, and analysis of real-time systems.

General Chair: Kang G. Shin, University of Michigan
Program Chair: Walter L. Helmerdinger, Honeywell, Inc.

Tuesday, December 1, 1987

REAL-TIME SCHEDULING - E. GAI
- Non-Preemptive Local Scheduling Policies for Hard Real-Time Distributed Systems - C. Woodside, D. Craig

FAULT TOLERANCE - J. GOLDBERG
- Optimized Reconfiguration Scheme for Fault-Tolerant MultiStage Networks - K. Yau, M. Malek
- Task Reconfiguration in a Distributed Real-Time System - R. Kieckhafer
- Design for Concurrent Error Detection in Storage/Logic Arrays - M. Spaulding, W. Fuchs

REAL-TIME ARCHITECTURES - E. SWARTZLANDER
- A Fixed Size Array Processor for Computing the Fast Fourier Transform - V. Balasubramanian, P. Banerjee
- A Technique for Reconfiguring Two Dimensional VLSI Arrays - F. Lombardi
- On O (Log n) Parallel Algorithm for Binary Multiplication and Its VLSI Implementation - B. Sinha, P. Srimani

REAL-TIME APPLICATIONS - W. MCDONALD
- RAS-M: A Multi/Microprocessor System for Dynamic System Simulation - V. Malbas
- MAX: Advanced General Purpose Real-Time Multiprocessor for Space Applications - R. Rasmussen, N. Dimopoulos, B. Bolotin, B. Lewis
- Using Replication for High Performance Database Support in Distributed Real-Time Systems - S. Son

Wednesday, December 2, 1987

REAL-TIME COMMUNICATIONS NETWORKS - A. MOK
- Hypervwitch Network for the Hypercube Concurrent Computer - E. Chow, H. Madan, J. Peterson
- Real-Time Correction by Intelligent Retry in Parallel Communication Networks - K. Yau, M. Malek
- Long-Term Reconfiguration of Double-Ring Networks Under Real-Time Constraints - H. Wedde, P. Tjole

REAL-TIME SYSTEM DESIGN - F. LOMBARDI
- Modelling, Specifying & Verifying Real-Time Embedded Computer Systems - J. Ostroff, W. Wonham

RUN TIME SUPPORT SOFTWARE - K. Wedde
- The Design of the Spring Kernel - J. Stankovic, K. Ramamritham
- Corest and Lace - T. Baker

REAL-TIME PERFORMANCE ANALYSIS - H. WEDDE
- Performance Analysis of Packet-switched Multiple-Bus Multiprocessor Systems - Q. Yang, R. Pavaskar
- Performance Analysis of Acknowledgment Mechanisms in Token-Bus Networks L. Ciminiere, A. Valenzano

REAL-TIME TIMING ANALYSIS - M. MALEK
- A Response Time Estimation of Real-Time Networks - T. As M. Yamashita, H. Matsumoto

Thursday, December 3, 1987

REAL-TIME PROGRAMMING - D. BHATT
- Imprecise Results: Utilizing Partial Computations in Real-Time Systems - K. Lin, S. Natarajan, J. Liu
- A Schedulability Analyzer for Real-Time Euclid - A. Stoyenko

DEVELOPMENT TOOLS - A. VAN TILBORG
- Melody: A Multiprocessor, Multiple Process Symbolic Debugger - K. Rowe, B. Pagurek, B. Vishnubhatla, J. Wilson, S. Boudouris
- An Instrumented Testbed for Real-Time Distributed Systems Development - D. Bhattacharjee, P. Ramanujan

REAL-TIME SCHEDULING - II - J. STANKOVIC
- Scheduling Real-Time, Periodic Jobs Using Imprecise Results - J. Liu, K. Lin, S. Natarajan
- Aperiodic Scheduling in a Hard Real-Time Environment - J. Lehoczky, L. Sha, J. Stroinski

To make reservations at the symposium hotel call or write directly to:
Eighth RTSS
Fairmont Hotel
211 South First Street
San Jose, CA 95113
(408) 996-1900

For more information contact:
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(313) 763-0361

Eighth Real-Time Systems Symposium
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MAIL REGISTRATIONS TO:
Eighth RTSS-87
Computer Society of the IEEE
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Advanced (prior to 11/18/87) 
Non-Member $135 
Student $60

Late (after 11/18/87) 
Non-Member $170 
Student $90

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