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About the Symposium

The International Symposium on Computer Architecture has established itself as the most important forum for introducing new ideas in computer architecture. This year, in addition to a rich technical program, the Symposium will feature two tutorials:

- Microprocessor Logic Design: The Flowchart Method, N. Tredennick, Nexgen, Sunnyvale
- Highly Parallel Systolic Arrays: Theory, Implementation and Application, H.T. Kung, Computer Science Department, Carnegie Mellon, Pittsburgh

The Symposium will be held at the Sheraton Hotel at Station Square, located near the scenic junction of Pittsburgh's three rivers. A visit to Carnegie Mellon campus will be organized. The registration fee includes lunches, a copy of the proceedings, and a reception. The student fee does not include the reception.

The Fourteenth International Symposium on Computer Architecture
June 2 - 5, 1987
Sheraton Station Square
Pittsburgh, Pennsylvania, USA

Advance Announcement

Make checks payable to 1987 Computer Architecture Symposium (U.S. currency only) and mail with this form to:
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Advance* On Site

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