### Recent IC Announcements

<table>
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<tr>
<th>MODEL AND FUNCTION</th>
<th>COMPANY</th>
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<tr>
<td>Br401, Br402, Br403, Br404 Pipelined SRAMs</td>
<td>Brooktree Corp. 9950 Barnes Canyon Rd. San Diego, CA 92121 (619) 452-7380</td>
<td>Designed for use as color palette RAMs in high-performance graphics systems, these four 256 x 8-bit devices feature 4-ns cycle times. The Br401 (100's price: $39) and Br402 (100's price: $39) offer 3 x 8-bit Sidecar RAMs; they are directly compatible with 10K ECL and 100K ECL, respectively. The Br403 (100's price: $36) and Br404 (100's price: $36) are functionally equivalent to the Br401 and Br402, respectively, but do not feature Sidecar RAM.</td>
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<td>82A436; 82C435 Enhanced graphics controller: bus interface</td>
<td>Chips and Technologies, Inc. 521 Cottonwood Dr. Milpitas, CA 95035 (408) 434-0600</td>
<td>Fully compatible with the IBM Enhanced Graphics Adapter, the 82C435 (100's price, $50) offers full backward compatibility to CGA, Hercules, and IBM Monochrome modes. It features 640 x 480 resolution and supports eight color planes. The 82A436 (100's price, $15) is implemented in a 68-pin PLCC package integrating bus interface logic, including buffers and drivers, memory and I/O select logic.</td>
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<td>Application-specific IC (ASIC) video controllers</td>
<td>Genoa Systems Corp. 73 E. Trimble Rd. San Jose, CA 95131 (408) 432-9090</td>
<td>Intended for OEs. Two proprietary VLSI CMOS families are offered. The first ($30 each in OEM volumes) has the gate array chip designed into Genoa's Spectrum video board products. Features include 132-column and 25/44 line text mode capability. The second ($50 each in OEM volumes) has the two-chip set designed into Genoa's Super EGA products. A 6845 CRTC is incorporated in the set.</td>
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<td>HADC77100A, HADC77100B A/D converters</td>
<td>Honeywell, Inc. Signal Processing Technologies 1150 E. Cheyenne Mountain Blvd. Colorado Springs, CO 80906 (303) 577-3511</td>
<td>These are 8-bit, monolithic, flash A/D converters. Both versions digitize a 2-volt analog input signal, with full Nyquist frequency components, at a nominal rate of 150 megasamples per second. The HADC77100A (100's price, $295) features 70 MHz full-scale bandwidth and ± ½ least-significant-bit linearity; the HADC77100B (100's price, $200) is available in ± ¼ LSB linearity.</td>
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<td>HS 9372K, HS 9372J, HS 9372TB, HS 9372SB DACs</td>
<td>Hybrid Systems Corp. 22 Linnell Circle/Suburban Industrial Park Billerica, MA 01821 (617) 667-8700</td>
<td>These are serial input, 16-bit, current output multiplying DACs. The four models, with the monotonicity (over temperature) and temperature range of each, are the HS 9372K, 16 bits min., 0°C to +70°C; the HS 9372J, 15 bits min., 0°C to +70°C; the HS 9372TB, 16 bits min., −55°C to +125°C; and the HS 9372SB, 15 bits min., −55°C to +125°C. They are priced at $65, $48, $150, and $125, respectively.</td>
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<td>HY51C256L, HY51256 DRAM</td>
<td>Hyundai Electronics America 4401 Great America Pkwy. 3rd Floor Santa Clara, CA 94043</td>
<td>Fabricated in HYCMOS technology, these 256K x 1 dynamic RAMs feature ripplemode operation. Standard access times available are 100, 120, and 150 ns. The HY51C256L (1000's price, $5) also features low standby current and a 32-ns extended refresh cycle. The HY51256 costs $3.50 (1000's).</td>
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<td>PLHS501 Random Logic Unit Programmable logic device</td>
<td>Signetics Corp. PO Box 3409 Sunnyvale, CA 94088-3409 (408) 991-2000</td>
<td>Based on a single NAND array with internal foldback paths and supported by Signetics' PC-based Amaze design automation software, this PLD implements logic functions utilizing DeMorgan's Theorem. Available in a 52-pin PLCC, the PLHS501 costs $26.99 (100's).</td>
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<td>TC511000P-85 DRAM</td>
<td>Toshiba America, Inc. 2692 Dow Ave. Tustin, CA 92680 (714) 832-6300</td>
<td>This 1M-bit x 1 dynamic RAM has an access time of 85 ns. Multiplexed address inputs permit packaging in a standard 18-pin plastic DIP. Features include read-modify-write, CAS before RAS refresh, RAS-only refresh, hidden refresh, and fast page mode capability. Contact a local sales office for 1987 prices.</td>
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<td>XC 2018 Logic Cell Array Programmable gate array</td>
<td>Xilinx, Inc. 2069 Hamilton Ave. San Jose, CA 95125 (408) 559-7778</td>
<td>The XC 2018 LCA contains the equivalent of 1800 conventional gates on one user-programmable chip. It operates at toggle rates up to 70 MHz and comes in an 84-pin package, or a 68-pin package with a pin-compatible upgrade for users of the XC 2064. The XC 2018 costs from $15 to $18 in production volumes.</td>
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