CALL FOR PAPERS

The 14th International Symposium on Computer Architecture

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Sheraton Station Square
Pittsburgh, Pennsylvania, USA

sponsored by

Computer Society of the IEEE
Institute of Electrical and Electronics Engineers (IEEE)
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Submitted papers will be accepted for evaluation until December 1, 1986. Five copies of the manuscript (in English, not exceeding 20 double-spaced pages) should be sent to:

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Notification of acceptance will be given by February 25, 1987. Authors of accepted papers will be required to submit a final, camera-ready copy by April 5, 1987.

Papers are solicited in any aspect of Computer Architecture. Topic areas include, but are not limited to:
- Architectures for artificial intelligence applications;
- Architectures for transaction-based systems;
- Language oriented architectures;
- Operating systems oriented architectures;
- Tools and methods for architecture design and description;
- Distributed and parallel architectures;
- Performance measurement and evaluation;
- Custom architectures;
- VLSI impact on architecture.

Selected papers will be nominated by the program committee for publication in the ACM Transactions on Computer Systems.