CALL FOR PAPERS
— 15th Annual Conference
1986 International Conference on Parallel Processing
August 19-22, 1986
Co-sponsored by
The Pennsylvania State University and the IEEE Computer Society
in cooperation with
Association for Computing Machinery

Authors are invited to submit papers describing recent advances on all aspects of parallel/distributed processing. These may include parallel/distributed logic circuits, impact of VLSI to parallel processor architecture; various concurrent-, distributed-, parallel-, pipeline-, or multiple-processor architectures; processor-memory interconnections; computer networks; distributed data bases; reliability and diagnostics; modeling and simulation techniques; performance measurements; operating systems; languages; or various application studies.

INSTRUCTIONS FOR AUTHORS
The conference will accept both regular and short papers. The deadline for submitting papers is February 1, 1986. For regular papers, four copies each of a 100-word abstract and the full text are required.

For short papers, authors should submit four copies each of a 100-word abstract and a summary of 1000 words.

Please make sure that summaries are of sufficient detail to permit careful evaluation by referees. Appropriate references and figures should be included in the summaries. Please include office and/or home telephone numbers. The papers should be submitted before February 1, 1986, to one of the following Technical Program Co-Chairpersons according to their areas of responsibility:

HARDWARE papers to:
Kai Hwang
Computer Research Institute, SAL-110
University of Southern California
Los Angeles, CA 90089-0781

SOFTWARE papers to:
Steven M. Jacobs
TRW Defense Systems Group
119/1842
One Space Park
Redondo Beach, CA 90278

ALL OTHER papers to:
Earl E. Swartzlander
TRW Electronics Systems Group
M5/1408
One Space Park
Redondo Beach, CA 90278

CONFERENCE PROCEEDINGS
The regular papers and the summaries of the short papers will be published in the conference proceedings. Special sheets for the preparation of accepted papers for the proceedings will be sent to each author.

CONFERENCE AWARDS
The conference will give two awards: one for Most Original Paper, the other for the Best Presentation.

CONFERENCE ENVIRONMENT
The Conference will be held at the Pheasant Run Resort in St. Charles, Illinois. Pheasant Run is only 45 minutes from Chicago’s loop and 25 miles from O’Hare International Airport. The Resort is situated on 300 acres in the beautiful Fox River Valley town of St. Charles, Illinois. The 550 rooms are homey and graceful with scenic views of a small lake or a beautiful golf course. On a clear day the Chicago skyline is visible from the upper levels of its sleek 15 story Tower. Special entertainment is available nightly. Pheasant Run Theatre features sumptuous dining followed by professional talent and critically acclaimed hit plays.

Resort facilities include 18 hole championship golf course, three swimming pools, indoor and outdoor tennis courts, Midwest’s most complete Health Spa and Fitness Center, Volleyball, Basketball and River Boat rides on the Fox River just 3 miles from the Hotel.

There is transportation available through the Hotel Transportation Department between Pheasant Run and O’Hare International Airport.

Informal, open bar gatherings will be held nightly for the conference participants.

A Conference brochure with pre-registration form and a technical program will be prepared and mailed in June of 1986.

TUTORIALS
At the request of many conference attendees, pre- and post-conference tutorials on parallel/distributed processing will be offered.

Submitted papers will be acknowledged promptly and authors will be notified of acceptance by April 1, 1986.