FEATURE ARTICLES

6  Guest Editors' Introduction: Computer Hardware Description Languages: The Bridge Between Software and Hardware
   Mario R. Barbacci and Takao Uehara

10 A Temporal Logic for Multilevel Reasoning About Hardware
   Ben Moskowski

22 Hardware Verification
   Fumihiro Maruyama and Masahiro Fujita

33 Concurrent Prolog as an Efficient VLSI Design Language
   Norihisa Suzuki

42 A Transformational Model of VLSI Systolic Design
   Monica S. Lam and Jack Mostow

55 Zeus: A Language for Expressing Algorithms in Hardware
   Steven M. German and Karl J. Lieberherr

67 Hardware Description Languages in Microprogramming Systems
   Subrata Dasgupta

81 The Conlan Project: Concepts, Implementations, and Applications
   Robert Piloty and Dominique Borrione

94 VHSIC Hardware Description Language
   Moe Shahdad, Roger Lipsett, Erich Marschner, Kellye Sheehan, Howard Cohen, Ron Wazman, and Dave Ackley

SPECIAL FEATURE

105 Parallelism, Pipelines, and Partitions: Variations on Communicating Modules
   Bernard L. Witt