YOU CAN’T MASK THE COST OF FATAL ERRORS ON THE CHIP

MASKAP finds them in the mask set. And that’s where errors can be found at lowest cost.

MASKAP finds errors other systems miss because MASKAP is the only fully-integrated, modular, data base system for complete mask analysis and verification. It actually models the fabrication process and extracts the circuit that will result if the IC is produced.

MASKAP performs a complete device-level verification of your chip’s logic as represented in the mask set. Usually in a single pass.

The MASKAP design rule checker handles all rules, including reflections and multiplexer interaction with no spurious errors. None.

MASKAP checks topology and detects and plots continuity errors, improperly formed devices and floating nodes. It isolates and pinspoints single or multi-layer shorts between major nodes, and checks electrical rules such as stuck or floating devices.

MASKAP is a complete data base system providing great application flexibility and absolute consistency of data. The system is both process and machine independent and extremely fast. CPU usage remains linear with chip size. MASKAP is user-friendly. It speaks and understands English, requires minimum inputs and delivers any desired output, at any time, printed and/or graphic.

MASKAP is doing the job for industry leaders. It can do the job for you.

We guarantee it.