The Second Annual Workshop on Microcomputer I/O, Peripherals and Software

April 10-11, 1979

Co-sponsored by: The Johns Hopkins University Applied Physics Laboratory

General Chairman: Paul L. Hazan
Program Chairman – I/O and Peripherals: Robert B. McDowell
Program Chairman – Software: Thomas Zaremba

PROGRAM

Tuesday, April 10, 1979 (9 a.m.)

1st SESSION – I/O Systems
Chairman: Ronald Imbriale, Tektronix, Inc.
Keynote: Does the Microprocessor have a Future as a Semiconductor Device . . .
Peter Verhofstadt, Fairchild Camera and Instrument Corp.
1. Overview – Interfaces on a Chip
   Carol A. Ogden, Software Technique, Inc.
2. An Advanced Intelligent I/O Subsystem on a Chip
   Khaled El-Ayat, Intel Corp.
3. Hybrid Chips
   Jeff Riskin, Analog Devices, Inc.
4. Distributed Microprocessor Systems
   Bernard Peuto, Zilog, Inc.
Panel Discussion: The Challenge of Smart I/O Chips

2nd SESSION – Standards
Chairman: Robert Carpenter, National Bureau of Standards
1. IEEE Microbus Standard
   Gordon Force, Logical Solutions, Inc.
2. The STD Bus
   Roger Born, Pro-Log Corp.
3. Processor/Peripheral Interface Bus Standard
   Hermann Schmid, General Electric Co.
Panel Discussion: Where Are We Going With Bus Standards?

Wednesday, April 11, 1979 (9 a.m.)

3rd SESSION – Software/Systems Development Tools and Techniques
Chairman: Tom Gabriel, Bendix, Inc.
1. Development Aids and Bus Analyzers
   Chuck House, Hewlett-Packard Co.
2. Universal Development Systems
   Bill Broderick, Tektronix, Inc.
3. Next Generation Microprocessor Vendor Support
   Len Bertain, Intel, Inc.
   Roger Born, Pro-Log Corp.
5. Universal Microsystem Analyzers
   Barney Hordos, Millennium Systems, Inc.
Panel Discussion: Does Industry Support Meet User Needs?

4th SESSION – Software/System Technology Trends
Chairman: Mars Gralia, APL
1. Intermediate Level Language Machines – “Pascal Microengine”
   Ed O'Neil, Western Digital Corp.
2. Systems Implementation Languages – Microforth
   Ed Conklin, Forth, Inc.
3. Software Productive Architectures
   6809 and 68000 Microprocessor Families
   Ian LeMair, Motorola, Inc.
4. High Level Language Support – TI Pascal Systems
   Kenneth Wickham, Texas Instruments, Inc.
Panel Discussion: Technology Trends – What, When and Why?

All attendees MUST preregister:
Attendance limited to 140
Make check payable to:
IEEE Microprocessor Workshop

IEEE and IEEE Computer Society Members – $20
Non-Members – $25
(Includes first day lunch)

Send registration to:
Mrs. Jacqueline Greene
The Johns Hopkins University
Applied Physics Laboratory
Johns Hopkins Road
Laurel, Maryland 20810
Tel: (301) 953-7100, Ext. 2160