Speed and THRUPUT . . .
That's CSPI'S Idea of
Array Processing

The Model A was faster than a horse and buggy, and it got you where you wanted to go.

A first generation array processor will compute and spit out data. It is faster than a minicomputer, but will it get you where you want to go on time? CSPI recognized that speed and throughput are the whole point of array processing. Therefore MAP™. The best fully-programmable, floating point array processor in the business. If you haven't seen MAP, you're only one step beyond the horse and buggy.

MAP is independent of its host for fast continuous data flow. MAP's internal controller and resident Snap II Executive frees the host to give 75 to 95% of its time to other tasks. Intelligent programmable interfaces and independent asynchronous multi-bus structure permit simultaneous input, processing, and output of data streams. There are no traffic jams with the MAP/host/peripheral union.

**Flexibility and economy are designed in.** You can mix and match 500, 300, and 125 nsec memory without interleaving or cycle stealing; you can address MAP directly in bytes, halfwords, or 32-bit words for memory efficiency. Modular architecture, devoid of synchronous clocks, lets you buy additional plug-in power when you need it: arithmetic processors, memories, multi/MAP configurations, and additional I/O processors transferring data up to 40 megabytes per second.

A total signal processing task can be programmed directly in FORTRAN. Any special array function can be programmed in 16-bit Assembly Language (not 64-bit microcode) and added to the MAP resident Snap II Executive.

Why not talk to someone who is getting fast throughput with MAP? Write CSPI for references as well as for technical specifications, price lists, and a configuration work sheet that helps you specify your own system.


cspi
CSP Inc. 209 Middlesex Turnpike, Burlington, MA 01803•617-272-6020
The leader in digital signal processing since 1968.
TWX: 710-332-0835

Reader Service Number 1