Join the group!

Help design and develop an LSI/Design Automation System for the '80s.

It's a rare opportunity, one that offers a great challenge and very real rewards. You can take part in developing a new systems concept, and follow it through to successful operations and maintenance. This opportunity is waiting for a few exceptional individuals at the Hughes Missile Systems Group in the San Fernando Valley area of Southern California.

The two illustrations below define the problem you will be facing, and the basic plan for meeting the challenge. One of the most exciting features of this intellectually stimulating program is that the solution will fulfill an eminently practical purpose in real life for the next decade and beyond.

This company-funded program is now underway, and has requirements in the following areas for people with specific or related experience at BS, MS and PhD levels.

**Simulator Design**
You will develop and maintain functional, logic and circuit simulators. Your responsibilities will include algorithmic development, software implementation, and user interface design, as well as Design Engineer one-to-one assistance.

**Automated Artwork Generation and Checking**
You'll be involved in developing and maintaining artwork design rule checking and mask level circuit regeneration software. Your responsibilities will include algorithmic development, software implementation and user interface design, as well as user assistance.

**Semiconductor Modeling and Analysis**
You will develop and maintain circuit-level semiconductor models, and will use those models to determine process design rules and standard LSI cells. Your responsibilities will include model development, model parameter determination, software implementation and circuit analysis, as well as Design Engineer one-to-one assistance.

Let us take action now with your resume. Please send it to: Engineering Employment, LSI-DA, Missile Systems Group, Fallbrook at Roscoe, Canoga Park, California 91304.

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**The problem and program goals.**

8 MONTHS

- Function Design → 10 LSI Chip Designs/System
- Logic Design → 10,000 Gates/Chip
- Mask Design → 100 Rectangles/Gate
- Processing → 10,000,000 Rectangles/System
- Testing → 100,000 Gates/System

**Our definition and solution.**

Automated Artwork Generation and Checking

Semiconductor Modeling and Analysis

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HUGHES
HUGHES AIRCRAFT COMPANY
U.S. Citizenship Required/Equal Opportunity
M/F/HC Employer