Sixth Texas Conference on 
COMPUTING SYSTEMS

14–15 November 1977

Sponsored by The University of Texas at Austin
In cooperation with

IEEE COMPUTER SOCIETY

The conference will cover a wide range of topics concerning computing systems with special emphasis on problems and applications of systems incorporating extremely large memories. The following is a partial list of papers to be presented at the conference.

Keynote address: “Advances in Software Engineering,” by Harlan Mills

MASS MEMORY SYSTEMS

“IBM II Mass Storage System” Willie Dowden
“IBM 3850”
“Cal Comp ATL”, Robert J. Cowen
“Optical Disk Memories” Mark Goldberg
“Electron Beam Erasable Memory” D.O. Smith
“Archival Electron Beam Memory” R.C. Raymond
“CCD Memories for Bulk Storage” A.F. Taesch, Jr., J.B. Barton, P.K. Chatterjee, H-S. Fu
“CCD Memories at Fairchild”
“Magnetic Bubble Memories for Military Applications 1985 and Beyond”
M.G. Mier, J.M. Blasingame, R.L. Johnson, S.E. Cummins, E.A. Buvinger
“Bubbles at Rockwell” George Pulliam
“Bubbles at TI” Don Klimus

SOFTWARE ENGINEERING

“Program Documentation and Data Representation” Yih-Wu Han
“A Case Study in Structured Programming Techniques Applied to Operating System Development” Jeffrey A. Karas, Donald G. MacKellar, Jr., Robert D. Hickey
“An Experiment in Software Error Data Collection and Analysis” N.F. Schneidewind, M.H. Hoffman
“Formal Verification in an Unverified Environment: An Experiment” David A. Patterson
“An Experience Data Base as an Aid to Software Development Estimation” Anthony A. Lekkos
“COMPUTER-TUTOR: A Student Designed CAI System” William H. Linder
“Software Engineering Education—A Constructive Criticism” Randall W. Jensen, Charles C. Tonies

OPERATING SYSTEMS

“Scheduling Processor Bound Systems” Deepak K. Goyal
“Automatic and General Solution to the Adaption of Programs in the Paging Environment” M.S. Achard, J.Y. Babonneau, M.G. Morisset, M.B. Mounajed
“Software Control of Digital Nodes: Handling 20-Megabit Throughput Rates in a Real-Time Network” E.E. Elenbaas
“A Model for Teaching Computer Architecture and Operating Systems” Mario J. Gonzales

ARCHITECTURE, HARDWARE
AND SIMULATION

“The effect of Interconnect Configuration on Delay in Computers Using High-Speed Circuits”
E.A. Wilson
“An AHPL Compiler/Simulator”
Robert E. Swanson, Zainalabedin Navabi, Frederick J. Hill
“Structure of a Problem-Oriented User’s Language for Automatic Engineering Design”
V. Radhakrishnamurthy, Kosumi N. Raja

There will also be sessions on DISTRIBUTED PROCESSING, DATA BASES, and ARTIFICIAL INTELLIGENCE.

The conference will be held at the Thompson Center on the campus of the University of Texas at Austin.

INDUSTRIAL SPONSORS: A company or government organization may purchase a block of conference tickets at a reduced rate. Reservations should be made by October 15. For more information, contact Alfred A. Dale, Department of Computer Science, University of Texas, Austin, Texas 78712, (512)-471-7316.


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September 1977