The IEEE Computer Society's Technical Committee on Minis and Micros presents

THE FIRST ANNUAL WORKSHOP ON

MICROCOMPUTER I/O, PERIPHERALS AND SOFTWARE

Co-sponsored by: The Johns Hopkins University Applied Physics Laboratory

General Chairman: Paul L. Hazan
Program Chairman — I/O and Peripherals: Robert B. McDowell
Program Chairman — Software: W. J. Sanda

Place: Johns Hopkins University Applied Physics Laboratory, Laurel, Md.
Date: October 11-12, 1977

PROGRAM

Tuesday, October 11, 1977 (9 a.m.)

1st SESSION
1. MICROCOMPUTER APPLICATIONS AND THE IMPORTANCE OF I/O
   David House, Intel
2. MICROCOMPUTER I/O ARCHITECTURE PHILOSOPHIES
   Thomas Zaremba, JHU/APL
3. INTERFACE DEVICES
   Gordon Force, National Semiconductor
Panel Discussion: I/O HARDWARE/SOFTWARE TRADEOFFS

2nd SESSION
1. I/O STANDARDS
   George Clark, National Bureau of Standards
2. PERIPHERALS
   Egil Juliiusen, Texas Instruments
Panel Discussion: WHAT I/O STANDARDS MEAN TO MICROPROCESSORS

Working Displays:
1. MICRO-CONTROLLED ARTIFICIAL ARM
2. IBM 360/91-TO-PROM PROGRAMMER
3. UNIVERSAL MULTI-CHIP DEVELOPMENT SYSTEM
4. INTERACTIVE PERSONAL COMPUTER

Wednesday, October 12, 1977 (9 a.m.)

1st SESSION — Development
1. MICROCOMPUTER DEVELOPMENT FACILITY
   Dr. R. Martinez, NOSC
2. MICROPROCESSOR LANGUAGES
   T. Dollhoff, Acuity Systems
3. SOFTWARE DEVELOPMENT SYSTEM FOR A BIT SLICE IMPLEMENTED ARRAY PROCESSOR
   R. S. Siebert, Collins Radio
Panel Discussion: FUNCTIONAL CHIPS-WAVE OF THE FUTURE?

2nd SESSION — Verification
1. MICROPROCESSORS—CONCEPT DESIGN AND SELECTION CRITERIA
   R. M. DeSantis, Hughes Aircraft
2. TEST AND VALIDATION OF MICROPROCESSOR SYSTEMS
   Dr. M. Graia, JHU/APL
3. SOFTWARE TOOLS FOR MICROPROCESSOR PROGRAMS
   J. Andersen, Univac
Panel Discussion: TOOLS FOR TESTING

For details write to IEEE Computer Society,
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Send registration to: Mrs. Jacqueline Greene
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All attendees MUST preregister: Attendance limited to 125.
IEEE and IEEE Computer Society Members — $20
Non-Members — $25
(Includes first day lunch)