TUTORIALS  Tuesday, September 6, 1977

Choice of 3 Topical Tutorials

- The Use of Precise Specification in the Development, Testing and Control of Software
- The Design and Application of Microprocessor Systems
- Distributed Processing: the Emerging Technology

The computer engineering conference for the decision-maker, the computer professional, the system user.
The Use of Precise Specification in the Development of Software

An exceptional offering for the professional programmer to upgrade his skills in a highly technical tutorial session.

David Parnas
University of North Carolina
David Gries
Cornell University

The Design and Application of Microprocessor Systems

The workshop will include a variety of problem solving exercises with 'hands-on' hardware for the participants.

Roger Westgate
Johns Hopkins University

Distributed Processing

Burt H. Leibowitz
International Computing Company
John Carson
RLG Associates

COMPCON 77 Fall offers the most comprehensive technical program attempted to date. Three superior tutorials covering the most topical subjects for the professional society membership are included. Extensive 'hands-on' opportunities as well as some demos will be featured throughout the conference.

For the first time, COMPCON 77 Fall will feature evening sessions. These evening sessions include a panel on DOD standardization; the National Software Works and a unique 'hands-on' session featuring personal computing.

Don't miss the informative and stimulating International Conference. Interact with the leaders in your own and related technical fields. See you at the Mayflower.
# The Use of Precise Specification in the Development of Software

**COURSE OUTLINE:**
The tutorial will cover the latest techniques in the use of precise specification in the development of software. Topics to be covered include:

- The division of software projects into work assignments (modules).
- The use of specification techniques to document decisions about the interfaces between the modules.
- The design of appropriately abstract interfaces.
- The use of correctness ideas in the development of correct programs from specifications.

**LECTURERS:**
- **Dr. David Parnas** is a Professor of Computer Science at the University of North Carolina at Chapel Hill. His work has been in the areas of software engineering and operating systems. He received the Ph.D. degree in systems and communications sciences from Carnegie Institute of Technology in 1965.
- **Dr. David Gries** is an Associate Professor of Computer Science at Cornell University. His work has been in the areas of programming methodology, programming languages design and compiler writing. He received the Dr. rer. nat. degree in mathematics from the Technische Hochschule Munich, Germany, in 1966.

# The Design and Application of Microprocessor Systems

**COURSE OUTLINE:**
The tutorial will cover a broad spectrum of microprocessor technology and applications. Topics to be covered include:

- Advanced microprocessor technology.
- Memory components and systems.
- Single chip processors.
- Interfacing to microprocessors.
- Programmable I/O chips.
- Bit-slice processors.
- Development systems.
- Software aids.

A ‘hands-on’ workshop will be a highlight of the microprocessor tutorial.

**LECTURER:**
- **Dr. Roger Westgate** is a Professor of Electrical Engineering at Johns Hopkins University. His work has been in the areas of electronic devices, circuits and microprocessors. He received his Ph.D. degree from Princeton University in 1966.

# Distributed Processing

**COURSE OUTLINE:**
The tutorial will address technological and managerial considerations in the design and application of distributed processing. Three broad categories of distributed systems will be discussed:

- Placing intelligence at the point of need.
- Resource sharing networks.
- Construction of Powerful Computing Systems by coupling multiple small computers.

**LECTURERS:**
- **Burt H. Liebowitz** is Vice President of International Computing Company. He has twenty years of experience in the computer field, the last five of which have been involved with distributed systems.
- **Dr. John Carson** is a computer scientist with RLG associates, Inc., Reston, Virginia. He is currently involved in the design and implementation of specially configured multiprocessor systems.

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**BOTH TUTORIALS START 9:00AM, TUESDAY, SEPTEMBER 6, 1977**

**REGISTRATION FOR TUTORIALS AND CONFERENCES ON LAST PAGE**
Wednesday, September 7, 1977

9:30 Plenary Session
Welcome and Awards
Merlin Smith, President, IEEE Computer Society
Paul L. Hazan, COMPON 77 FALL General Chairperson
Paul S. Skartvedt, COMPON 77 FALL Program Chairperson
Conference Keynote Address

11:00 Technical Keynote Sessions (Parallel)
Isaac Auerbach, President, Auerbach Publications
Microprocessors: Microprocessors, Their Impact on Design Engineering
Eugene McFarland, Asst. V.P., MOS Division, Texas Instruments

11:30 Technical Keynote Sessions (Parallel)
Computer Applications: Application Trends and Directions
Joe M. Henson, Vice President, Market Planning, Data Processing Division, IBM
Component Technology and Memories: Trends in Future Memory Development
Tom Klein, National Semiconductor Corp.

12:00 Technical Keynote Sessions (Parallel)
Distributed Processing: An Overview of Distributed Processing
E. Douglas Jensen, Honeywell Systems & Research Center, Minneapolis, Minnesota
Peripherals: Peripherals in the Future

12:30 Technical Keynote Sessions (Parallel)
Software: Can Software Be More Like Hardware? Should It Be?
Walter Beam, U.S. Air Force, Office of the Assistant Secretary, Deputy for Advanced Technology
System Technology: Trends in Computer System Technology and Architecture
Edson De Castro, President, Data General Corp., Boston, Massachusetts

1:00 LUNCH

Wednesday Afternoon (Parallel Sessions)

2:30 Session 1 (Software)
Software Personnel Development
Chairperson: D.S. Johnson, Texas Instruments
"Air Force Continuing Education in Software Acquisition and Engineering," Capt. J.B. Peterson, AFIT, Wright Patterson AFB

Session 2 (Component Technology and Memories)
Topics in Memory Technology
Chairperson: Joel Karp, Ram Power Inc., Palo Alto, California
"Bubble Memory Performance in System Design," Paul White, Burroughs Corp., MCO, San Diego, California

Session 3 (Distributed Processing)
Hierarchical Configurations
Chairperson: Grayce Booth, Honeywell Information Systems
"Hierarchical Configurations for Distributed Processing," Grayce Booth, Honeywell Information Systems
"Some Reality to Hierarchical Systems," David Stone, GE Telecommunications & Information Systems

4:00 Session 4 (System Technology)
Array and Directed Applications
Chairperson: Howard Johnson, System Development Corp.
"The Array 2 Processor," Paul B. Schneek, NASA
"A Pipelined Architecture Bit-Slice Computer for High Level Language," Jean Pierre Schoellkopf

Session 5 (Applications)
Automatic Texture Analysis of Medical Images
Chairperson: R. Ledley, National Biomedical Foundation, Georgetown University
"Texac: A Special Purpose Picture Processing Texture Analysis Computer," R.S. Ledley and L.S. Rotolo, National Biomedical Foundation
"Interactive Image Processing Systems," C. Harlow, L. Cook, P. Cook, S. Dwyer, University of Missouri

Wednesday Evening
5:30 COMPON Cocktail Party
7:00 DOD Standardization Panel

Thursday, September 8, 1977

Thursday Morning (Parallel Sessions)

9:00 Session 7 (Software)
Software Management Discipline

Session 8 (Peripherals)
State of the Art Storage Technology and Devices
Chairperson: L.J. Mathews, Magnetic Peripherals, Inc.
"Thin Film Technology for Heads and Disks"
"Moving Head, Fixed and Removable Media, Disk Storage Trends"
"New Mass Storage Approaches and Impact on a Current Disk Drive"

Session 9 (System Technology)
Implementation Techniques for Next Generation Systems
Chairperson: J.R. Leonard
"DBS Architecture and the Functions It Should Support," D.Z. Badal, University of California, Los Angeles
"Multimicroprocessor Architectures and the Use of Multilevel Encoding in Microinstruction Formats," C.V.W. Armstrong
SESSION 10 (MICROPROCESSORS)
HIGH LEVEL LANGUAGES FOR MICRO
Chairperson: Terry Dolhoff, Acuity Systems
"SYSTEMS LANGUAGES, WHERE DO THEY FIT?"
"BASIC: THE HOBBY STANDARD"
"ASSEMBLY LANGUAGE – FOR BETTER OR FOR WORSE,"
Terry Dolhoff, Acuity Systems

SESSION 11 (DISTRIBUTED PROCESSING)
MODELS AND SIMULATION FOR DISTRIBUTED PROCESSING
Chairperson: Stephen R. Kimberston, NBS
"RESULTS OF A GPSS SIMULATION OF THE AFOS CLOSED
LOOP STORE-AND-FORWARD COMMUNICATIONS
NETWORK," M.S. Goldman and Dr. S.N. Wei,
National Weather Service, Silver Spring, Maryland
"DESCRIPTION AND REALIZATION OF PARALLEL CONTROL
SYSTEMS," M. Courvoisier, Centre National De La Recherche
Scientifique, France
"JOB ALLOCATION IN A DISTRIBUTED COMPUTER NETWORK,"
Glenn R. Linsenmayer and P. Ligomenidis,
Glen Burnie, Maryland

SESSION 12 (SOFTWARE)
SOFTWARE ENGINEERING AND DEVELOPMENT FOR
MICROPROCESSORS
Chairperson: Barry Press, TRW Defense & Space Systems Group
"MICROPROCESSOR SOFTWARE ENGINEERING,"
Col. F.J. Hilbing, Rome Air Development Center, USAF
"A HIGH LEVEL MICROPROCESSOR PROGRAMMING
LANGUAGE," D.F. Furgeron and A.J. Gibbons,
Westinghouse Electric Corp.
"USING HIGH LEVEL LANGUAGES TO PRODUCE LOAD
MODULES FOR ROM IN MEDIUM (2000) QUANTITIES,"
L. Saunders, Tetronix, Inc.

1:00 LUNCH

THURSDAY AFTERNOON (Parallel Sessions)

SESSION 13 (MICROPROCESSORS)
MICROCOMPUTER DEVELOPMENT TECHNIQUES
Chairperson: Theodore Powell, Texas Instruments
"HELP FOR MICROPROCESSOR SOFTWARE DEVELOPMENT,"
H.A. Cohen and R.S. Francis, Latrobe University,
Australia
"INTEGRATING MICROCOMPUTERS HARDWARE AND SOFTWARE
DEVELOPMENT TOOLS," Marvin Conrad, Texas Instruments
"UNIVERSAL MICROPROCESSOR SYSTEMS SOFTWARE,"
W.H. Burkhart, Universitaet Stuttgart, Hzenbergstr

SESSION 14 (SYSTEM TECHNOLOGY)
MINI AND MULTI PROCESSORS
Chairperson: Bill Poduska, Prime Corp.
"TRENDS IN SUPERMINI ARCHITECTURE," B. Tannenbaum,
& S. Wallach, Data General Corp.
"IBM SERIES ONE – AN ARCHITECTURE OVERVIEW," Stu
Elder, IBM
"THE MULTIPROCESSOR SYSTEMS SMS 201," R. Kobert

SESSION 15 (COMPONENT TECHNOLOGY AND MEMORIES)
DIRECTIONS OF LOGIC TECHNOLOGY
Chairperson: L.M. Terman, IBM
Dick Pashley, INTEL Corp.
Tom Longo, Fairchild Corp.
W.V. Lin, AMDAHL Corp.

SESSION 16 (APPLICATIONS)
MILITARY APPLICATIONS
Chairperson: J.M. Wilcox, Computer Sciences Corp., Virginia
"APPLICATIONS OF MICROPROGRAMMABLE COMPUTERS
FOR MILITARY SYSTEMS"
"APPLICATION OF EMULATION FOR MILITARY SYSTEMS
VALIDATION, VERIFICATION AND INTEROPERABILITY TESTING"
"EMERGING SOFTWARE DEVELOPMENT METHODOLOGIES
FOR MILITARY SYSTEMS DEVELOPMENT

SESSION 17 (APPLICATIONS)
DIGITAL SIGNAL PROCESSING
Chairperson: R.L. Carberry, IBM
"DEVELOPMENTS IN SIGNAL PROCESSING ALGORITHMS,
"T. Kriz, IBM
"SIGNAL PROCESSING ARCHITECTURAL CONSIDERA-
TIONS," P. Kogge, IBM
"TECHNOLOGY REQUIREMENTS FOR SIGNAL PROCESSING,
"J. Phillips, TRW

SESSION 18 (SOFTWARE)
MODERN PROGRAMMING PRACTICES
Chairperson: R.W. Weber, ISIM Rome Air Development Center
"EFFECTS OF MODERN PROGRAMMING PRACTICES ON
SOFTWARE DEVELOPMENT COSTS," R.K.E. Black,
Boeing Computer Services
"IMPACT OF MODERN PROGRAMMING PRACTICES ON
SYSTEM DEVELOPMENT," J.R. Brown, TRW Defense and
Space Systems Group
"AN EVALUATION OF THE EFFECTIVENESS OF SOFTWARE
ENGINEERING TECHNIQUES," P.C. Belford, Computer
Sciences Corp.

THURSDAY EVENING
5:30 COMPCON Cocktail Party

THURSDAY EVENING PARALLEL SESSIONS

SESSION 19 (SOFTWARE)
The National Software Works: Development of Operational Issues
Chairperson: R.A. Robinson, Information Sciences Division,
Rome Air Development Center
"NATIONAL SOFTWARE WORKS – OVERVIEW & STATUS,"
R.A. Robinson, Information Sciences Division, Rome Air
Development Center
"SOFTWARE CONFIGURATION MANAGEMENT USING
OPERATING SYSTEM PRIMITIVES OF THE NATIONAL
SOFTWARE WORKS," N.L. Rasmussen, Gagliari Systems
Group, Inc.
"SOFTWARE DEVELOPMENT FOR MINICOMPUTERS USING
NATIONAL SOFTWARE WORKS TOOLS," S.L. Fleischman,
Gagliari Systems Group, Inc.
"THE NATIONAL SOFTWARE WORKS: A USER APPRAISAL,"
N.W. Peterson, TRW Defense & Space Systems Group

SESSION 20 (SPECIAL EVENING FEATURE)
PERSONAL COMPUTING – A 'HANDS ON' SESSION AND WORKSHOP

FRIDAY September 9, 1977

FRIDAY MORNING (Parallel Sessions)

SESSION 21 (COMPONENT TECHNOLOGY AND MEMORIES)
MEMORY SYSTEMS
Chairperson: Bob Welch, National Semiconductor Corp.
"STATIC OR DYNAMIC, WHICH TO USE FOR MY MEMORY
SYSTEM?" J.C. Blackie, National Semiconductor Corp.
"PSEUDO RANDOM ACCESS MEMORY SYSTEM WITH CCD-SR
AND MOS RAM ON A CHIP," Naoya Ohno and
Katsuya Hakozaki, Nippon Electric, Japan
"ADVANTAGES OF CCD SYSTEMS IN FUTURE MACHINE
ARCHITECTURE," Joe Miller, INTEL Corp.

SESSION 22 (PERIPHERALS)
SMART PERIPHERALS
Chairperson: J.S. Toreson, Microcomputer Systems Corp.
"STANDARD INTERFACES FOR MINICOMPUTERS," Gary
Robinson, Inforex
"INTELLIGENT DISK, THE NEXT GENERATION," Mark Fuller,
Warco
"TERMINALS AND DATA ENTRY," Steve Star, Hewlett Packard
9:30 SESSION 23 (DISTRIBUTED PROCESSING)
NETWORKS FOR DISTRIBUTED PROCESSING
Chairperson: Stuart Wecker, Digital Equipment Corp.
"ACKNOWLEDGE ETHERNET," Mario Tokuru and Kiichiro Tamaru, Keio University, Yokohama, Japan
"A NETWORK COMPUTER FOR DISTRIBUTED PROCESSING," W. Huen, Illinois Institute of Technology

10:00 SESSION 24 (SOFTWARE)
SOFTWARE RELIABILITY ANALYSIS
Chairperson: Martin Shooman, Polytechnic Institute of New York
"A QUANTITATIVE CONNECTION BETWEEN COMPUTER PROGRAMS AND TECHNICAL PROSE," M. Halstead, Purdue University, Lafayette, Indiana
"MEASURING PROGRAM COMPLEXITY," J. Zolnowski, Texas A&M University, College Station, Texas
"STATISTICAL THEORY OF COMPUTER PROGRAM’S INFORMATION CONTENT AND COMPLEXITY," A. Laennel, Polytechnic Institute of New York

SESSION 25 (MICROPROCESSORS)
MICROPROCESSOR BASED CONTROLLERS
Chairperson: Van Lewing, Fairchild Semiconductor
"CONTROL OF SYNCHRONOUS COMMUNICATIONS USING MICROCOMPUTER LSI PERIPHERALS," J.W. Thomas, Texas Instruments
"A MICROPROCESSOR DESIGN FOR VEHICLE CONTROL," Neal Laurance, Ford Motor Company
"A BIT-SLICE MODULE SET FOR MICROCOMPUTING," Richard J. Smith, RCA

10:30 SESSION 26 (APPLICATIONS)
REAL-TIME APPLICATIONS
Chairperson: Robert New, Singer-Link Division
"THE SPACE SHUTTLE GROUND CONTROL SYSTEM/A NEW REAL-TIME CHALLENGE," G.H. Evans, IBM, Houston
"REAL-TIME REDUNDANCY MANAGEMENT FOR DIGITAL FLY-BY-WIRE AIRCRAFT CONTROL," E.A. Megna, MIT, and K.J. Szewczyk, NASA

LUNCH (12:30 and 1:00)
FRIDAY AFTERNOON (Parallel Sessions)
2:00 SESSION 26 (COMPONENT TECHNOLOGY AND MEMORIES)
USAGE OF MEMORY COMPONENTS
Chairperson: Tom Dalfi, Intersil, Sunnyvale, California
"CCD APPLICATION," Kurt McKinsey, INTEL Corp.
"4K CMOS STATIC RAM," Shep Hume, Intersil Corp.
"ELECTRONIC MEMORY HIERARCHY," Dick Bravo, Intersil Corp.

SESSION 27 (SOFTWARE)
SOFTWARE ENGINEERING & DEVELOPMENT FOR DISTRIBUTED PROCESSING SYSTEMS
Chairperson: Clarence Giese, Ballistic Missile Defense Systems Command, Huntsville, Alabama
"SOFTWARE FOR DISTRIBUTED MICROCOMPUTER SYSTEMS," C. Whitby-Stevens, University of Warwick, England
"DERIVATION AND EVALUATION OF DISTRIBUTED COMPUTER ELEMENTS USING A DIGITAL DESIGN LANGUAGE," R.D. Ippolito, Carnegie-Mellon University, Pittsburgh, Pennsylvania

SESSION 28 (MICROPROCESSORS)
SYSTEM ENGINEERING WITH MICROCS
Chairperson: Herb Arkin, Singer-Kearfott
"MULTI PROCESSOR WITH QUEUE MEMORIES," Donald R. Mott and A. Schmitz, General Electric Co.
"PROMOS – A DISTRIBUTED MICROPROCESSORS SYSTEM," Joseph C. Lombardi, U.S. Steel
"MICROPROCESSOR AVIONICS APPLICATION," N.C. Joehlin, Singer Kearfott

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- TUTORIAL registration fee includes luncheon and bound text.
- COMPCON registration fee includes one copy of the Digest of Papers, and provides you with 2 complimentary drink tickets for each of the COMPCON hosted parties, Wednesday and Thursday nights.

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