FTCS-7
1977 International Symposium on Fault-Tolerant Computing

ACCOMMODATIONS

Write directly to: University Hilton
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Los Angeles, CA 90007
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*Be sure to register by June 5 and mention FTCS-7

FTCS-7 SEVENTH INTERNATIONAL SYMPOSIUM ON FAULT-TOLERANT COMPUTING

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$65 IEEE Member ($80 after June 4)
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Covers session attendance, copy of conference proceedings, 2 lunches, 1 dinner & cocktail party, and coffee breaks.

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University Hilton Hotel
LOS ANGELES, CA. • JUNE 28-30, 1977

This year's major forum for technical interchange on every significant aspect of fault-tolerant computing will be held June 28-30 in Los Angeles, California. DON'T MISS IT!

Sponsored by the IEEE Computer Society's Technical Committee on Fault-Tolerant Computing.

In July 1978, the IEEE Transactions on Computers will have a Special Issue devoted to Fault-Tolerant Computing. The approximate due date for submission of papers is August 1, 1977. Further details will be answered at FTCS-7.

Submit papers to: Dr. S. Reddy
University of Iowa
Division of Information Engineering
Iowa City, Iowa 52242
JUNE 28 – TUESDAY MORNING
Opening remarks
General Chairman: A.D. Friedman
Program Chairman: J.P. Hayes

Session 1 – Reliability and Availability
1. “Reliability models for multiprocessor systems with and without periodic maintenance,”
   A.D. Ingle and D.P. Siewiorek (Carnegie-Mellon University).
2. “Reliability and availability modeling of systems featuring hardware and software faults,”
   C. Landrault and J.C. Laprie (LAAS, Toulouse).
4. “A reliability model for gracefully degrading and repairable fault-tolerant systems,”
   Y.W. Ng (Bell Laboratories) and A. Avizienis (UCLA).

JUNE 28 – TUESDAY AFTERNOON
Session 2 – Fault-Tolerant Systems
1. “C. vmp: the architecture and implementation of a fault-tolerant multiprocessor,”
3. “Development of a spaceborne computer with a single error and erasure correction scheme,”
   C.J. Black (British Aircraft Corp.), C.E. Lundberg (Lund University) and W.K.S. Walker (ESTEC, Noordwijk).

Session 3 – System-Level Diagnosis
   (University of Wisconsin).
2. “System diagnosis with t/s diagnosability,”
   S. Karunanithi and A.D. Friedman (USC).

JUNE 29 – WEDNESDAY MORNING
Session 4 – Testing: Deterministic Approaches
   (University of Illinois).
   (ENSIMAG, Grenoble).
4. “Generation of tests for digital circuits using generalized fault lists,” Y. Levendel (Bell Laboratories) and M.A. Breuer (USC).
5. “Local irredundancy in combinational circuits,” Y.W. Ng (Bell Laboratories) and A. Avizienis (UCLA).

JUNE 29 – WEDNESDAY AFTERNOON
Session 5 – Self-Checking Systems
2. “Design of totally self-checking asynchronous and synchronous sequential machines,”
   F. Özgüner (Technical University of Istanbul).
   and A.D. Friedman (USC).

JUNE 30 – THURSDAY MORNING
Session 6 – Testing: Probabilistic Approaches
2. “Signal reliability of combinational and sequential circuits,” I. Koren (University of California, Santa Barbara).
5. “Optimal random testing of single intermittent failures in combinational circuits,”
   J. Savir (Stanford University).

JUNE 30 – THURSDAY AFTERNOON
Session 7a (Short Papers) – Circuits and Coding
Session 7b (Short Papers) – Systems and Software

LOS ANGELES, CALIFORNIA
June 28-30, 1977 FTCS-7