DISE WORKSHOP ON MICROPROCESSORS AND EDUCATION
AUGUST 16-18, 1976
COLORADO STATE UNIVERSITY
FORT COLLINS, COLORADO

Sponsors: The DISE* Committee and the Electrical Engineering and Computer Science Departments of Colorado State University

SCOPE:
This workshop is specifically intended for participation by educators involved in digital systems education. The purposes are: to present the fundamentals of and latest developments in microprocessors; to enable educators to focus on the impact of microprocessors on curricula; and to identify what is needed to effectively use microprocessors in an educational environment.

FEATURES:
VIDEO-TAPES: Video-tapes will be available covering the fundamentals of microprocessors, several microprocessor architectures and the use of microprocessors in logic design. These videotapes have been prepared by Dr. Adam Osborne and follow his books, *An Introduction to Microcomputers* (two volumes) and *8080 Programming for Logic Design*. Copies of all three volumes will be mailed to each registrant so that they may be studied prior to attending the workshop.

PROTOTYPE SYSTEMS: Prototype systems and field engineers from Fairchild, Intel, National, IMS Associates, E & L Instruments, and Brigham Young University will be available each day and evening of the workshop, to enable workshop attendees to gain "hands-on" experience with one or more of these representative systems. Nine books or manuals from these and other manufacturers will be supplied to each workshop participant.

TECHNICAL PROGRAM:
Session 4: "Microprocessor Software," Chairman, Yaohan Chu, University of Maryland.
Session 5: "The Impact of Microprocessors on the Curriculum," Chairman, David Irwin, Auburn University.
Session 6: "Microprocessor Laboratories," Chairman, Peter Rony, VPI.
Session 7: "Microcomputer Systems and Applications," Chairman, Bala Parrasuman, Syscom, Inc.
Session 8: Discussion Groups.
Purpose: To build on information presented in the previous seven sessions, to involve and take advantage of knowledge, expertise, and opinions of workshop participants in order to produce consensus reports of the needs of the educational community in each area.

REGISTRATION:
Because of the unique features of the workshop (the videotapes and prototype systems) attendance will have to be limited to 100 attendees on a first-come first-serve basis. The registration fee for the workshop is $90.00. A check payable to "DISE Microprocessor Workshop" the registrant's Name, Affiliation and Address should be sent to:

DISE Workshop on Microprocessors and Education
Department of Electrical Engineering
University of Pittsburgh
Pittsburgh, PA 15261

*The DISE (Digital Systems Education) Project is sponsored by Grant #GZ-2997 from the National Science Foundation.