The digest of papers presented at the Ninth Annual Design Automation Workshop under joint sponsorship of the ACM and the IEEE on June 26-28, 1972, Dallas, Texas. Topics include: Turnkey Systems; Packaging; Testing and Simulation; Mechanical Design Automation; Architecture; Design Automation Systems; Microprogramming; Interactive Graphics; General Design Automation.
Price—$12.50 Member Price—$10.00

A digest of the eight papers presented at this one-day in-depth symposium. Paper topics: Optical Computing by Holography; Pattern Recognition by Holography; Optical Memories; Real-Time Optical Processing Devices; Holographic Computing in Radar and Ultrasons; Digital Computer and Optical/Processor Interactions; Introducing the Laser Computer of Today and the Atomic Computer of Tomorrow; and Digital Holographic Logic.
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The papers represent recent advances in both the theory and applications areas of computer processing of pictorial information. The 40 papers provide a stimulating variety of theoretical and applied topics in two-dimensional digital signal processing, pictorial pattern recognition, two-dimensional filtering, texture measurement, image communication theory and image analysis with applications to radiographic, geophysical, biomedical, photo reconnaissance, image communications and environmental problems. Many illustrations.
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The Computer Society's fifth annual conference focuses on subjects related to the development and use of quantitative methods for designing and evaluating computer systems and their components, be they hardware, software or firmware, or combinations thereof. Some of the topics the 96 presented papers cover are: Billing for Timesharing; External Signal Processing (FFT); Cellular Logic; Computers in Transportation Systems; Computer-Aided Design of Computers; Fault Diagnosis; Microprogramming Techniques; Teleprocessing; Microprogrammed Peripheral Controllers; Memory Mapping Techniques; System Measurement Techniques; Minicomputer Applications; and the Centralized vs. Decentralized File Systems; Computer Networks – Military and Civil; and Fundamental Limits of Computing.
Price—$23.00 Member Price—$19.50

This one-day symposium covered: Memory Cells and Technology; Memory Module Organizations; Overall Memory Module Considerations; and Logic From Memory. The symposium was organized to try and find solutions to LSI problem areas such as establishment of a design technology; packaging and power dissipation; high volume production of many parts; establishment of system reliability; testing requirements and standards; and establishment of real memory component and system costs.

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This seminar investigates computer interface aspects, computers and test systems, computers in process control and a variety of other topics. Paper topics include: Computer-Instrument Systems – Are We Interface Limited?; Interfacing Instrumentation to Timeshared Computers; Common Computer Interfaces; Applications of a New “Mini-Automator” Interface to Computerized Testing; Computer-Aided Pollution Abatement; Hardware/Software Characteristics of a Computer Controlled Test System; and Application of Digital Computers to Process Control.

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The Computer Society's second annual conference examines in depth the impact that LSI electronics will have upon the information processing profession. Areas covered include: Questions We Should Ask; The Changing User-Supplier Interface; Semiconductor Memory for Computer Systems — What We Can Expect; The Impact of LSI on Computer Organization; Impact on the Profession and the Professionals and Engineering and Business Perspectives.

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The Computer Society's first annual conference explores in depth the areas of reliability; design automation; pattern recognition; and new computer elements and system organizations. Session topics include: Reliability; Computer Systems Design Automation (a tutorial); The Role of Electrical Engineers in Computer Science; Computer-Aided Circuit and Device Design; Computer Memories; Pattern Recognition; Computer Science in Electrical Engineering Curricula; New Computer Elements; and System and Logic Design Automation.

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