SEMICONDUCTOR MEMORY TEST SYMPOSIUM

"TESTING TO INTEGRATE SEMICONDUCTOR MEMORIES INTO COMPUTER MAINFRAMES"

OCTOBER 4TH 1972, RICKSHAW INN., CHERRY HILL, N.J.

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A PANEL OF EXPERTS WILL DISCUSS PRACTICAL TEST SOLUTIONS TO THE VARIED PROBLEMS ENCOUNTERED BY THE MANUFACTURER AND USER OF SEMICONDUCTOR MEMORIES.

MAJOR MANUFACTURERS AND USERS WILL DISCUSS THE TEST RELATED PROBLEMS THAT HAVE TO BE OVERCOME TO SUCCESSFULLY INCORPORATE SEMICONDUCTOR MEMORIES INTO THE COMPUTER MAINFRAME.

LEADING MAKERS OF MEMORY TEST EQUIPMENT WILL PRESENT THE TEST PHILOSOPHY FOR THE ECONOMIC SOLUTION OF THESE PROBLEMS.

AN OPEN DISCUSSION BETWEEN THE PANEL AND THE AUDIENCE WILL FOLLOW EACH PRESENTATION AND THE SYMPOSIUM WILL BE CONCLUDED WITH TIME ALLOWED FOR GENERAL DISCUSSION IN AN INFORMAL ATMOSPHERE.

INDEPENDENT EXHIBITS OF VARIOUS MEMORY TEST PRODUCTS WILL BE CONDUCTED AFTER THE SYMPOSIUM.

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