methods for designing and evaluating computer systems and their components, be they hardware, software or firmware, or combinations thereof. Some of the topics included in the proceedings are: Billing for Timesharing; External Signal Processing (FFT); Cellular Logic; Computers in Transportation Systems; Computer-Aided Design of Computers; Fault Diagnosis; Microprogramming Techniques; Teleprocessing; Microprogrammed Peripheral Controllers; Memory Mapping Techniques; System Measurement Techniques; Minicomputer Applications; Centralized vs. Distributed File Systems; Computer Networks—Military and Civil; and Fundamental Limits of Computing.

Price—$20.00 Member Price—$12.00


The Computer Society's fourth annual conference emphasizes the engineering and design aspects of memories and peripherals with papers covering new contributions, state-of-the-art, projections, evaluations, applications and surveys of the field. Session topics include: Tomorrow's Technology and Markets; Packaging for Electronic Systems; Computer Power in Terminals; New Memory Devices; High Risk Money for New Computer Companies: How, When and Where; Main Frame Memory Technology: A Debate; Terminals for News Processing and Editing; Special Purpose Terminals and Peripherals; Low Cost Teledisplays; Software and Terminals; and many others.

Price—$8.00 Member Price—$6.00


The Computer Society's third annual conference explores the world of real time systems. Sessions topics include: Computer Data Communications; Computer System Models and Analysis; Process Control and Digital Filtering; Time Sharing Systems, Large and Small; Real Time Systems for the Management of Acutely Ill Patients; On Line Computer Systems in Hospital Data Management; Real Time Operating Systems and Languages; New Developments in Software and Systems; and Biomedical Computing.

Price—$7.50 Member Price—$5.00

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The Computer Society's second annual conference examines in depth the impact that LSI electronics will have upon the information processing profession. Areas covered include: Questions We Should Ask; The Changing User-Supplier Interface; Semiconductor Memory for Computer Systems — What We Can Expect; The Impact of LSI on Computer Organization; Impact on the Profession and the Professionals and Engineering and Business Perspectives.

Price—$7.00 Member Price—$5.00


The Computer Society's first annual conference explores in depth the areas of reliability; design automation; pattern recognition; and new computer elements and system organizations. Session topics include: Reliability; Computer Systems Design Automation (a tutorial); The Role of Electrical Engineers in Computer Science; Computer-Aided Circuit and Device Design; Computer Memories; Pattern Recognition; Computer Science in Electrical Engineering Curricula; New Computer Elements; and System and Logic Design Automation.

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12th Annual Symposium on Switching and Automata Theory Conference Record, October 1971 — 218 p.

Papers were selected for the symposium on the basis of their originality and relevance to the problems of current interest (some represent preliminary reports of continuing research) in switching and automata theory. Just a few of the topics covered are: high level languages of maximum power; design of easily testable sequential machines; decreasing computing time for modular arithmetic; two-dimensional formal languages; and pattern recognition by cellular automata; priority paging algorithms and the extension problem; time bounds on space computations; economy of description by automata; grammars and formal systems; and computing the maximum and the median.

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Papers were selected for the symposium on the basis of their originality and relevance to the problems of current interest (some represent preliminary reports of continuing research) in switching and automata theory. Paper titles include: Phenomenon in the Theory of Sorting; Program Schemata as Automata; Tape-Bounds for Time-Bound Turing Machines; Fault Detection Experiments for Asynchronous Sequential Machines; Syntactic Clues; Writing Stack Acceptors; Series-Parallel Irreducibility: Machine Oriented Definitions and Proofs; and Parsing Algorithms with Backtrack.

Price—$15.00 Member Price—$7.50


Those individuals engaged in computer
communication system design, analysis, and research, participated in this symposium. The sessions cover such topics as ANSI standards, systems aspects, software, modeling, and networks.

Price—$15.00 Member Price—$10.00

The one day symposium covered: Memory Cells and Technology; Memory Module Organizations; Overall Memory Module Considerations; and Logic From Memory. The symposium was organized to try and find solutions to LSI problem areas such as establishment of a design technology; packaging and power dissipation; high volume production of many parts; establishment of system reliability; testing requirements and standards; and establishment of real memory component and system costs.

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The extent and effectiveness of the interplay that exists between computer designers and users during the development of a new computing system is the basis for the papers presented at this symposium. Several papers include preliminary reports of on-going work. Topics include: Meta-Compiler Use in Machine Design; Instruction Deferral Sequencing Mechanism; Interrupt and Attention Handling in High Level Programming Languages; Programming Language Directed Machine Design – Problems and Prospects; A Computing Machine Based on Tree Structures; and The Programmer as a Computer Designer.

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Papers on work accomplished and largely unreported during the last three to four years by research workers in every major effort were invited for this state-of-the-art symposium. Session topics include: test generation and diagnosis; fault-location and testing; diagnosis and testing; reliability modeling and analysis; architecture and design; error protection and recovery; and software reliability.

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This seminar investigates computer interface aspects, computers and test systems, computers in process control and a variety of other topics. Paper topics include: Computer- Instrument Systems – Are We Interface Limited?; Interfacing Instrumentation to Timeshared Computers; Common Computer Interfaces; Applications of a New “Mini-Automator” Interface to Computerized Testing; Computer-Aided Pollution Abatement; Hardware/Software Characteristics of a Computer Controlled Test System; and Application of Digital Computers to Process Control.

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The symposium provided a forum for those interested in pattern recognition to present their results, explore new developments in depth, and discuss the directions of further work. The 29 full papers are included in the following sessions: Theoretical Aspects; High Energy Physics Applications; Digital Processing and Analysis of Biomedical Images; Optical Character Recognition; Picture and Image Processing; and Speech Signal. Thirty-four short papers were covered in three additional sessions.

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