WORKSHOP

Terminal-Oriented Systems


Recognizing the projected growth in terminal-oriented systems, we intend to examine:

- The demands made by future terminal-oriented systems on hardware components.
- The application of current and projected techniques in future systems.
- The importance of such concepts as intelligent terminals, communication processors and remote multiplexing.
- Principal terminals of the future.

Emphasis will be in major design considerations and trade-offs, including the significant motivating and limiting factors.

The orientation of the workshop is mainly toward the hardware requirements; however, areas having potential hardware/software trade-offs will be of particular interest as well. Other software problems are also of interest, particularly as it relates to hardware requirements; e.g., storage, memory cycles, performance.

Sessions are planned in the following areas: the characterization of existing or projected terminal-oriented systems; specific design questions of particular interest in terminal-oriented systems; terminals; memory requirements in terminals and terminal systems; a summary or "wrap-up" session.

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International Conference on Pattern Recognition

A three-day conference sponsored by the Pattern Recognition Committee of IEEE Computer Group, Pattern Recognition Conference of IEEE SSC Group and the University of Hawaii will be held in Honolulu, Hawaii on January 18-20, 1971.

The conference chairman is C. K. Chow of the Computer Group Recognition Committee and the local chairman is M. S. Watanabe of University of Hawaii.

International Symposium on Fault-Tolerant Computing

March 1-3, 1971
Huntington-Sheraton Hotel,
Pasadena, Calif.

Beside the four previously announced sessions (Computer Group News, July/Aug. 1970, p. 31), a new session has been added to the Symposium:
Session V: Software Reliability—Program Validation, Testing, and Debugging.
Co-Chairmen: Dr. B. Elspas and Dr. K. N. Levitt (Stanford Research Institute, Menlo Park, Calif. 94025, Tel. (415) 326-6200).

Authors interested in contributing papers to this session are requested to contact the co-chairmen immediately if a late submission is contemplated. The general deadline for the submission of complete drafts for all sessions is November 1, 1970; however, extensions may be allowed by session chairmen.

As of October 1, about 50 authors had indicated intentions to contribute papers to the Symposium. The large number of proposed contributions to Session II (Fault Location and Diagnosis) is expected to lead to the formation of a new Session VI, dealing with applications of diagnosis and the testing of large logic arrays.

The International Symposium on Fault-Tolerant Computing is co-sponsored by the Technical Committee on Fault-Tolerant Computing of the IEEE Computer Group and the Jet Propulsion Laboratory (JPL), California Institute of Technology. Information on the symposium can be obtained from the General Chairman, Prof. Algirdas Avizienis (UCLA Computer Science Dept., Los Angeles, Calif. 90024). Inquiries relating to the technical program should be directed to the Program Committee Chairman, Dr. W. C. Carter (IBM Research Center, P.O. Box 218, Yorktown Heights, N.Y. 10598).

Chairman of the Local Arrangements Committee is Mr. William F. Scott (JPL), the Publications Committee is headed by Dr. G. C. Gilley (JPL), and the Registration Committee by Mr. D. K. Rubin (JPL). The address for all three Committees is: spacecraft Computers Section, Jet Propulsion Laboratory, Pasadena, Calif. 91103.

The January/February 1971 issue of COMPUTER magazine will be a special issue on fault-tolerant computing. It will contain the complete technical program (including abstracts) of the Symposium and details on reservations, registration, and other local arrangements. Survey papers covering the major fields of fault-tolerant computing will be featured in this issue.

A Conference Digest containing four-page summaries of all papers will be provided to the registrants at the Symposium. Complete papers will be reviewed for publication in a special issue of the IEEE Transactions on Computers, which is being planned by its Guest Editor, Prof. C. V. Ramamoorthy (University of Texas at Austin, Austin, Texas 78712).

Design Automation Workshop

Proceedings of the 6th Annual SHARE-ACM-IEEE Workshop held June 8-12, 1969, 414 pp. Paperback, $10. Available from ACM, 1133 Avenue of the Americas, New York, N.Y. 10036, prepaid. 33 papers were presented at 11 sessions devoted to interconnection design; mechanical design; digital logic; structural analysis; circuit analysis; artwork generation; interconnection data system; simulation; digital systems; and graphics. Papers of particular interest include a discipline for generating practical optimal structural designs; design automation in the sheet metal shop; and automated design of open web steel joints.