MIP-512 - The Single Board Multibus Machine Vision System

- 512 x 512 resolution
- 8 bits/pixel up to 24 bits/pixel
- 4:3 or 1:1 aspect ratio
- 16.7 million color LUT
- Hardware pan, scroll and zoom
- Dual ported video RAM for true DMA
- IEEE-796 (Multibus) compatible
- 8 bit frame grabber
- 90ns/pixel video ALU
- IP software library (C, Fortran)
- Image processing functions:
  - addition & subtraction
  - averaging
  - convolution (N x M)

The MIP-512 supports instantaneous pixel by pixel panning and scrolling of images, and zooming by a factor of x2 or x4. Up to 256 colors or shades of gray can be displayed from the 16.7 million color LUT.

REAL-TIME IMAGE PROCESSING
A 90ns/pixel video ALU is provided for performing real-time arithmetic and logical operations on image data. The MIP-512 supports several standard image processing functions including frame addition and subtraction, exponentially weighted frame averaging, low and high pass filtering and convolution with an N x M kernel. All computations are performed at high speed by the MIP-512 without burdening the host CPU.

In addition to state-of-the-art graphics and imaging boards, Matrox also supplies monitors, cardcages, CPU cards, memory boards and communication controllers for complete OEM display system requirements.

Call now for a complete documentation package.