For tough image processing problems like pixel rotation, picture regeneration, or hidden line removal from wire frame models, Mini-MAP gets results in seconds, not hours. Attach a Mini-MAP, model MM-111, to a PDP-11 UNIBUS and you have an interactive number cruncher that is ideal for image processing, CAD/CAM, solid modeling, simulation, and animation.

Shared memory simplifies programming and provides the unprecedented throughput necessary for truly interactive image processing of complex algorithms. 32-bit floating point arithmetic, along with 7 MFLOPS of number crunching power, adapts to any input data format with ease.

Shared memory enhances performance and makes the unique hardware interface between the host and Mini-MAP an integral part of the system. The UNIBUS interface between Mini-MAP and the host 100 percent cuts down I/O time and keeps image processing going at full speed.

A scientific subroutine library of FORTRAN callable routines including an expanding selection of image processing algorithms is available for Mini-MAP. For optimum performance, high-level FORTRAN control languages are provided for both the host and Mini-MAP.

Memory is expandable up to 16 MBytes. Configurations include a four-board set with DEC-type backplane or fully packaged systems.

System integrators are finding Mini-MAP is the most cost-effective number crunching solution for image manipulation. Write for information or call on our applications engineers for assistance.

- 32-bit floating point precision
- Shared memory UNIBUS interface
- 150 FORTRAN callable arithmetic routines
- Up to 16 MBytes of memory
- 1024 x 1024 2-D real FFT in 8.8 seconds
- 1280 x 1024 4-color image rotation (Raster Scan Storage Format) in 27.5 seconds.

DEC, PDP-11, and UNIBUS are trademarks of Digital Equipment Corp. Mini-MAP and MM-111 are trademarks of CSPI.

CSPI
THE ARRAY PROCESSORS
40 Linnell Circle, Billerica, Massachusetts 01821 • 617/272-6020 • TWX: 710-347-0176
Reader Service Number 8