

# Vincent Gaudet, Ph.D., P.Eng., SMIEEE

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### Biosketch

Vincent Gaudet (S'97 M'03 SM'10) received the B.Sc. degree in Computer Engineering from the University of Manitoba in 1995, the M.A.Sc. from the University of Toronto in 1997, and the Ph.D. from the University of Toronto in 2003. From 2002 to 2010, he was on faculty at the University of Alberta. Currently, he is a Professor in the Department of Electrical and Computer Engineering at the University of Waterloo (Waterloo, Canada), and serves as Associate Chair for Undergraduate Studies. In 2002, he was a Research Associate at Télécom-Bretagne (Brest, France). In 2008-2009 he was on sabbatical at Northeastern University (Boston, MA), and at the Research Institute of Electrical Communication in Tohoku University (Sendai, Japan).

His research interests focus on information-processing microsystems, more specifically on energy-efficient microelectronic circuits for graph-based decoding of error-control codes. He is interested in biomedical circuits and systems, stochastic computation, and multiple-valued logic.

Prof. Gaudet is licensed as a Professional Engineer in Ontario. He has served as Associate Editor for the IEEE Transactions on Circuits and Systems, Parts I and II, and has been a Technical Editor for the IEEE International Solid-State Circuits Conference since 2009. He is Vice-Chair of the IEEE Computer Society Technical Committee on Multiple-Valued Logic and is an Associate Editor for the Journal of Multiple-Valued Logic and Soft Computing. He was the Program Chair for the 2012 IEEE International Symposium on Multiple-Valued Logic (ISMVL) in Victoria, BC, and the General Chair of ISMVL 2015 in Waterloo, ON. He has been the Corresponding Guest Editor of an IEEE Journal of Selected and Emerging Topics in Circuits and Systems (JETCAS) special issue on MVL. He was the recipient of the 2009 Petro Canada Young Innovator Award. Through Oxford University Press, he has co-authored a laboratory manual to accompany the Microelectronic Circuits textbook by Sedra and Smith.

### Position Statement

The multiple-valued logic (MVL) community is diverse and engaged, and includes researchers from many walks of life, ranging from philosophy and algebra all the way to microelectronic circuits and next-generation devices. Despite the range of seemingly disparate disciplines, I always find it striking just how much the attendees at the International Symposium on Multiple-Valued Logic (ISMVL) come together as a group. As opposed to a "multi-conference" with several parallel, unrelated discussions, ISMVL truly focuses on MVL, and everyone contributes their own particular knowledge to a global discussion.

Maintaining and promoting this rich culture of inclusiveness will be key in upcoming years. Many areas of research are faced with decreased funding resources, making it more difficult for some people to justify their attendance. Losing attendees from less resource-rich disciplines would inevitably lead to a less enriching symposium. To help out our community, I think we will have to look at ways to keep registration costs down, while continuing to host top-notch events. This is not an easy problem to tackle! Also, the MVL community has always been good at mentoring its next-generation researchers. We will also have to continue doing this by promoting more student activities such as poster sessions.