TCPP Newsletter

Technical Committee on Parallel Processing

July 2013

This issue's highlights:

- News
- Awards list from sponsored conferences.
- Faculty, and postdoctoral open positions .
- IPDPS 2013 update with special issue on sponsored workshops.
- Upcoming TCPP sponsored and related meetings. .

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Editor-Vinay B Gavi-rangaswamy

Report on IPDPS 2013, held in Boston, USA

27th IEEE International Parallel & Distributed Processing Symposium Front Desk.

Recently held TCPP's flagship conference *"27th IEEE International Parallel & Distributed Processing Symposium"* concluded eventfully.

IPDPS 2013 was hosted at Hyatt Regency Cambridge, located next to MIT campus. Event kicked off with welcome reception and TCPP annual meeting by Ajay Gupta, Western Michigan University. He specifically noted IPDPS's significance as its flagship conference and featured highest number of workshops compared to all other TCPP sponsored conferences. David A. Bader, Georgia Institute of Technology was recognized for his outstanding service during banquet and the 2013 IEEE Reynold B. Johnson Information Storage Systems Award was presented to Michael L. Kazar by IEEE President Peter Staecker.

This year's invited talks were thought provoking and full of ambitions as ever. Shekhar Borkar's discussion on Exascale performance highlighted need to achieve high level of parallelism while using energy saving techniques, whereas James Dmmel explained about arithmetic and communication cost that is involved in numerical techniques involving n-bodies. He also focused on techniques developed to reduce energy cost. And while Josh Simons contemplated about HPC Cloud being good or bad; his talk emphasized on how HPC community should actively participate in evolution of enterprise computing alongside mainstream HPC.

This year event highlighted total of 22 TCPP sponsored workshops and a PhD forum held in conjunction with the conference. Pradeep Padala from VMware discussed research challenges faced by virtualization community for Dynamically Distributed Resource Management. Elsevier Author Workshop on Scientific Publishing was notably helpful to next generation researchers (PhD students), it helped them avoid pit falls and also gave guidance in publishing their work. This year IPDPS also introduced a special PhD student mentoring session to help them with research dos and don'ts specific to individual attendees.

Invitation for articles

As a continual effort to improve the newsletter content we are thinking to include technical articles related to parallel processing and high performance computing. In this effort we are inviting all of you to contribute to the TCPP newsletter by submitting good, informative articles that you come across or short write-ups on the latest trends and technologies. These can be distributed with both the monthly newsletter and the forum section on the TCPP website.

In News

NSF Parallel Reconfigurable Observational Environment (PRObE) Cluster Resource Availability

NSF's PRObE (www.nmc-probe.org) operates four clusters to support systems research at scale. The largest is Kodiak (https://www.nmc-probe.org/wiki/Machines:Kodiak), which is 1000 nodes (two core x86, 8GB DRAM, two 1TB disks, 1GE and 8Gbps IB) donated by Los Alamos National Laboratory.

Today Kodiak is hosting researchers from Georgia Tech, Carnegie Mellon and Los Alamos. Princeton researchers have published results from Kodiak at the most recent NSDI (Wyatt Lloyd, "Stronger Semantics for Low-Latency Geo-Replicated Storage", NSDI 2013). On PRObE staging clusters are researchers from University Central of Florida, University of Texas, Austin, Georgia Tech and Carnegie Mellon. PRObE resources are intended for (infrastructure) systems researchers committed to public release of their research results, typically publishing in distributed systems (eg. OSDI or SOSP), cloud computing (e.g. SOCC), supercomputing (e.g. SC or HPDC), storage (e.g. FAST), or networking (e.g. NSDI). PRObE resources are managed by Emulab (www.emulab.org) a cluster manager for allocating physical nodes that has been in use for systems research for over a decade (Brian White, "An Experimental Environment for Distributed Systems and Networks," OSDI 2002). Users start by porting and demonstrating their code on a 100-node staging cluster such as Denali built from the same equipment donation from Los Alamos. With demonstrated success on a staging cluster, and a compelling research goal, Kodiak can be requested and allocated, possibly exclusively, for hours to days. To start using PRObE resources:

- Visit www.nmc-probe.org to learn about the resources
- Visit portal.nmc-probe.org to request a PRObE-specific Emulab account
- Have a research leader or faculty member get an account and define a project on portal.nmc-probe.org
- Use Portal to get onto Denali, to allocate a single node experiment, login into that node to customize and resave the OS image for your
- Project, then launch a multi-node experiment to demonstrate your system at <100 scale
- -use https://www.nmc-probe.org/request/ to request a large allocation on Kodiak (this is a HotCRP paper review web site, where your paper is a short justification for your research, your preparedness for using Kodiak, and your credentials and appropriateness for using NSF resources)
- PRObE managers will review, approve and schedule your use of large allocations of Kodiak time

In a matter of weeks another style of large PRObE resource will come online. Susitna is 34 nodes of 64 core x86 processors, for a total of more than 2000 x86 cores. Susitna also has NVidia donated K20 GPU coprocessors with 2496 CUDA cores each, for a total of 84,864 cuda cores. With 128 GB DRAM, a hard disk and an SSD each, Susitna nodes are interconnected by 40Gbps Ethernet, 40 Gbps infiniband and 1Gbps ethernet. All uses of PRObE resources are obligated to publish their results, either in conferences or one their web sites, and acknowledge NSF PRObE resources used in these publications.

Deadline: resource is available during 2013 and 2014

Webpage: http://www.nmc-probe.org/

Contact: probe@newmexicoconsortium.org

Also see: www.cs.cmu.edu/~garth/papers/07_gibson_036-039_final.pdf

Advanced Computational Infrastructure Open for Research

Two NSF-supported advanced computational infrastructure facilities were recently formally declared open for use by the science and engineering research community at-large.

- Stampede at the University of Texas at Austin's Texas Advanced Computing Center (TACC),
- Blue Waters at the NationalCenter for Supercomputing Applications at the University of Illinois at Urbana-Champaign

These two major facilities are part of the NSF's comprehensive strategy for advanced computing infrastructure to enable transformative foundational research in computational and data-intensive science and engineering across all disciplines.

To apply to use these facilities, please see:

Stampede and other resources: https://www.xsede.org/using-xsede

Blue Waters: http://www.nsf.gov/funding/pgm_summ.jsp?pims_id=503224

Awards from sponsored conferences

PerCom 2013 Awards

Marc Weiser Best Paper Award

"METIS: Exploring Mobile Phone Sensing Offloading for Efficiently Supporting Social Sensing Applications" Kiran K. Rachuri (University of Cambridge, UK); Christos Efstratiou (University of Cambridge, UK); Ilias Leontiadis (University of Cambridge, UK); Cecilia Mascolo (University of Cambridge, UK); Peter Rentfrow (University of Cambridge, UK)

Ten Years CoMoRea Impact Paper Award

Xiaohang Wang, Daqing Zhang, Tao Gu, Hung Keng Pung, "Ontology Based Context Modeling and Reasoning using OWL," IEEE PerCom 2004 CoMoRea Workshop, 2004

Best Demo Award

"Reinventing the Share Button for Physical Spaces" Darren Carlson (University of Lübeck, Germany); Bashar Altakrouri (University of Lübeck, Germany); Andreas Schrader (University of Lübeck, Germany)

Best Work in Progress Paper

"I see you: How to Improve Wearable Activity Recognition by Leveraging Information from Environmental Cameras" Gernot Bahle (DFKI, Germany); Paul Lukowicz (DFKI and University of Kaiserslautern, Germany); Kai Kunze (University of Passau, Germany); Koichi Kise (Osaka Prefecture University, Japan)

Best Ph.D. Forum Paper

"Toward Large-scale Situation Awareness Applications on Camera Networks" Kirak Hong (Georgia Institute of Technology, USA)

DCOSS 2013 Awards

Best paper award

"The Problem Bit ", Matthias Keller, Jan Beutel, Lothar Thiele

Certificate of Appreciation

Kaushik R. Chowdhury

IPDPS Awards

Babbage Award

James Demmel, University of California at BerkeleyIn recognition of his innovative contributions to the development of linear algebra algorithms and software.

PhD Forum Best Poster Awards

Abhishek Gupta, University of Illinois at Urbana-Champaign (Towards Efficient Mapping, Scheduling, and Execution of HPC Applications on Platforms in Cloud)

Ardavan Pedram, University of Texas at Austin (Algorithm/Architecture Codesign of Low Power and High Performance Linear Algebra Compute Fabrics)

Thilo Schöndienst, University of Massachusetts Dartmouth (Reducing the Environmental Impact of Optical Networks)

TCPP Outstanding Service Award

David A. Bader of Georgia Institute of Technology,

The 2013 IEEE Reynold B. Johnson Information Storage Systems Award

Michael L. Kazar by IEEE President Peter Staecker

Special Report on IPDPS 2013 Workshops

IPDPS '13 hosted in total 22 workshops along with main conference. These workshops give conference diversity through discussion of "preliminary and cutting-edge" work than main stream conference. This also allows people to pursue topic of special interest. Workshops are held on first and last day of the conference.

Each year IPDPS encourages people to submit "proposal for new workshops"; and this year deadline is on August 1, 2013.

APDCM

The main goal of this workshop is to provide a timely forum for the exchange and dissemination of new ideas, techniques and research in the field of the parallel and distributed computational models. The workshop is meant to bring together researchers and practitioners interested in all aspects of parallel and distributed computing taken in an inclusive, rather than exclusive, sense. We are convinced that the workshop atmosphere will be conducive to open and mutually beneficial exchanges of ideas between the participants.

In past new computational devices and environments such as optical interconnects, programmable logic arrays, networks of workstations, radio communications, mobile computing, DNA computing, quantum computing, sensor networks etc. have also been focus of this workshop. Selected paper is published in International Journal of Networking and Computing .

DPDNS

Increasingly large and complex parallel, distributed and network-centric computing systems provide unique challenges to the researchers in dependable computing, especially because of the high failure rates intrinsic to these systems. The goal of this workshop in continuation of the FTPDS (Fault- Tolerant Parallel and Distributed Systems) workshop series is to provide a forum for researchers and practitioners to discuss all aspects of dependability including reliability, availability, safety and security for parallel, distributed and network-centric systems. All aspects of design, theory and realization are of interest.

HCW

Heterogeneous computing systems comprise growing numbers of increasingly more diverse computing resources that can be local to one another or geographically distributed. The opportunity and need for effectively utilizing heterogeneous computing resources has given rise to the notions of cluster computing, grid computing, and peer-to-peer computing. HCW encourages paper submissions from both the research and industry communities presenting novel ideas on theoretical and practical aspects of computing in heterogeneous computing environments.

The HCW 2013 proceedings will be published through the IEEE Computer

AsHES

The AsHES'13 workshop provided a forum for researchers and practitioners involved in application and architecture studies about hybrid systems. The workshop kicked off with a keynote speech given by Dr. Richard Vuduc (Georgia Tech), on design implications of heterogeneity according to first principles of algorithms and architectures. Afterwards, 11 authors presented their work. Paper topics covered various areas of hybrid systems, including programming models, performance optimizations, accelerated applications, and emerging hybrid systems.

CloudFlow

The CloudFlow workshop is a venue for the workflow and Cloud communities to define models and paradigms, present their state-of-the-art work, share their thoughts and experiences, and explore new directions in realizing workflows in the Cloud. CloudFlow2013 was the second time we held the workshop series, and we invited two distinguished keynote speakers, one was Professor and chair of the department of computer science at Georgia State University, the other, Péter Kacsuk, Professor and head of the Laboratory of Parallel and Distributed Systems (LPDS), Computer and Automation Research Institute of the Hungarian Academy of Sciences. Professor Pan's keynote on Cloud Programming Models was very well received as he described the language and model evolution in distributed systems and pointed us towards a viable X2M (translate x into MapReduce) approach. Professor Kacsuk's work on running meta-workflows on top of multiple heterogeneous Cloud environments also had large user base and applications across the world. For details of the workshop, please visit http://cloud-uestc.cn/cloudflow/program.html.

EduPar '13

The Third NSF/TCPP Workshop on Parallel and Distributed Computing Education (EduPar-13) (http://cs.gsu.edu/~tcpp/curriculum/?q=EduPar-13_Proceedings) was a success with a popular keynote on MOOC from David Luebke of NVIDIA, an invited talk by Dan Katz of NSF, 13 contributed papers, 14 lightning talks, and 21 posters. The attendance was between 35-60 folks (and 5-10 on webinar) throughout the day, causing us to switch to a larger room.

About two dozen early adopters of NSF/TCPP Curriculum Initiative on Parallel and Distributed Computing were awarded travel support sponsored by NSF and Intel. The CFP is out for NSF/TCPP CDER Center Early Adopter Awards for Fall -13 (http://www.cs.gsu.edu/~tcpp/curriculum/?q=Fall2013_CFP) with June 30

HiCOMB

High-performance computing became an integral part of research and development in bioinformatics and computational biology. The large size of biological data sets, inherent complexity of biological problems and the ability to deal with error-prone data, all result in large run-time and memory requirements. The goal of this workshop is to provide a forum for discussion of latest research in developing high-performance computing solutions to data-intensive and compute-intensive problems arising from molecular biology and related life sciences areas. We are especially interested in parallel and distributed algorithms, memory-efficient algorithms, large scale data mining techniques, including approaches for big data and cloud computing, algorithms on multicores and GPUs, and design of high-performance software for biological applications. The workshop will feature contributed papers as well as invited talks from reputed researchers in the field.

HIPS

The 18th HIPS workshop served as a forum for research in the areas of parallel applications, language design, compilers, runtime systems, and programming tools. In our call for papers, we especially encouraged innovative approaches in the areas of emerging programming models for large-scale parallel systems and many-core architectures. This year, we received 29 quality submissions and accepted 9 (31%). The workshop was productive and was kicked off with a provocative keynote given by Michael Heroux of Sandia National Laboratories who outlined needed (and unneeded) technologies in programming models and supportive environments for exascale computing from an application developer's perspective. The three paper sessions focused on support for accelerator and many-core architectures, scalable tools for correctness and performance analysis, and programming models and abstractions.

HPGC

This year we had 8 contributed papers, which were organized into two sessions. In addition we invited two speakers to present two poles of the cloud spectrum. Norm Rubin from NVIDIA talked about gaming in the cloud and Rajkumar Kettimuthu from Argonne National Lab presented Globus Online. Over the years we have seen the evolution from grid to cloud computing, which is reflected in this year's contributions, which all concerned cloud computing. The two sessions addressed a range of issues related to high performance cloud computing, including GPUs, loop scheduling, storage, integration with grid middleware, data analysis in the cloud and management of virtual machines.

MTAAP '13

The MTAAP 2013 workshop was a full-day meeting to be held at the IPDPS 2013 focusing on Multithreading Architectures and Applications. This workshop intends to identify applications that are amenable to MT, MIC, and hybrid programming and execution models, as well as the underlying architectures on which they can thrive. The workshop seeks to explore programming frameworks in the form of languages and libraries, compilers, analysis and debugging tools to increase the programming productivity.

ParLearning '13

Parlearning is an IPDPS workshop with special emphasis on the HPC techniques for emerging machine learning and data mining algorithms on massive dataset. Those techniques have drawn extremely high attention in both HPC and ML communities and form a cornerstone of the Big Data analysis. ParLearning'13 is featured by two keynote speeches titled "Large Scale Data Analytics: Challenges, and the role of Stratified Data Placement" and "Parallel Methods for Bayesian Network Structure Learning", delivered by Prof. Srinivasan Parthasarathy at the Ohio State University and Prof. Srinivas Aluru at the Iowa State University, respectively. As a workshop in an interdisciplinary research area, ParLearning'13 brought the researchers from both communities successfully, which fosters potential collaborations and inspires novel thoughts. We look forward to further

PDSEC '13

Parallel and Distributed Scientific and Engineering Computing is a special workshop will bring together computer scientists, applied mathematicians and researchers to present, discuss and exchange ideas, results, work in progress and experiences in the area of parallel and distributed computing for problems in science and engineering applications and inter-disciplinary applications.

HPDIC

The 2nd International Workshop on High Performance Data Intensive Computing (HPDIC2013) was held in Boston on May 24,2013 in conjunction with IEEE IPDPS 2013. The HPDIC workshop received 46 submissions worldwide and attracted many attendees during the workshop. Dr. Xavier Guerin from IBM T.J. Watson Research Center made a keynote speech entitled "Next-Generation Enterprise Computing using Hardware-Accelerated Network Fabrics ".

All the presented papers were dispatched in 3 sessions,i.e.,MapReduce, Hadoop and New Architecture Support for Data Intensive Computing,File System and Storage for Data Intensive Computing, and Data Analytics: Solutions and Case Studies. The paper "I/O Containers: Managing the Data Analytics and Visualization Pipelines of High End Codes" co-authored by Jai Dayal, Jianting Cao, Greg Eisenhauer, Karsten Schwan, Matthew Wolf, Fang heng(Georgia Institute of Technology),Hasan Abbasi, Scott Klasky, Norbert Podhorszki (Oak Ridge National Labs), and Jay Lofstead (Sandia National Labs) won the best paper award among the 20 accepted papers (see the attached picture). The workshop chairs are also working on journal special issues for selected papers from this year's workshop and the future HPDIC workshop series.

HPPAC

The 9th IEEE Workshop on High-Performance, Power-Aware Computing (HPPAC) provided a forum for the exchange and dissemination of new ideas, techniques, and research in high-performance, power-aware computing. The workshop attracted wide participation with contributions by researchers from national laboratories, academia and industry. Accepted papers this year covered topics including power-efficient hardware, power measurement and profiling, large-scale power management, and compiler/runtime techniques. The keynote presentation by Dr. Tali Moreshet from Swarthmore College provided an overview of energy-efficient synchronization mechanisms, including locks, barriers, and transactional memory. The workshop attracted a consistently large audience and continues to foster a vibrant community.

LSPP

The workshop on Large-Scale Parallel Processing is a forum that focuses on computer systems that utilize thousands of processors and beyond. Large-scale systems, referred to by some as extreme-scale and Ultra-scale, have many important research aspects that need detailed examination in order for their effective design, deployment, and utilization to take place. These include handling the substantial increase in multi-core on a chip, the ensuing interconnection hierarchy, communication, and synchronization mechanisms. Increasingly this is becoming an issue of co-design involving performance, power and reliability aspects. The workshop aims to bring together researchers from different communities working on challenging problems in this area for a dynamic exchange of ideas. Work at early stages of development as well as work that has been demonstrated in practice is equally welcome.

Of particular interest are papers that identify and analyze novel ideas rather than providing incremental advances in the areas of Large-scale systems, Multi-core, Novel architectures and experimental systems, Energy Management, Warehouse Computing, and Applications.

Results of both theoretical and practical significance will be considered, as well as work that has demonstrated impact at small-scale that will also affect large-scale systems. Work may involve algorithms, languages, various types of models, or hardware

PCO

The workshop on new trends in Parallel Computing and Optimization aims at providing a forum for scientific researchers and engineers on recent advances in the field of parallel or distributed computing for difficult combinatorial optimization problems, like 0-1 multidimensional knapsack problems and cutting stock problems, large scale linear programming problems, nonlinear optimization problems and global optimization problems. Emphasis will be placed on new techniques for the solution of these difficult problems like cooperative methods for integer programming problems and polynomial optimization methods. Aspects related to Combinatorial Scientific Computing (CSC) will also be treated. Finally, the use of new approaches in parallel computing like GPU or hybrid computing, peer to peer computing and cloud computing will be considered. Application to planning, logistics, manufacturing, finance, telecommunications and computational biology will be considered.

PLC

This workshop provides a forum for the presentation of research on all aspects of heterogeneous systems programming models, compiler optimizations, language extensions, and software tools for such systems. Selected papers will appear in the special issue of IEEE Journal

RAW

The 20th anniversary of RAW edition in Boston has been very successful. This year workshop featured two prestigious keynote speaker Dr. Ivo Bolsens as and Dr. Jaime Cummins, CEO of Pico Computing. Workshop topics of interest include research in all areas of reconfigurable systems, including architectures, algorithms, applications, software and cross-cutting areas.

NIDISC

Techniques based on metaheuristics and nature-inspired paradigms can provide efficient solutions to a wide variety of problems. Moreover, parallel and distributed metaheuristics can be used to provide more powerful problem solving environments in a variety of fields, ranging, for example, from finance to bio- and health-informatics.

This workshop seeks to provide an opportunity for researchers to explore the connection between metaheuristics and the development of solutions to problems that arise in operations research, parallel computing, telecommunications, and many others. Topics of interest include, but are not limited to:

Nature-inspired methods for problem solving environments. Parallel and distributed metaheuristics techniques (algorithms, technologies and tools). Applications combining traditional parallel and distributed computing and optimization techniques as well as theoretical issues. Other algorithms and applications relating the above mentioned research areas.

ViPES

The 1st Workshop on Virtual Prototyping of Parallel and Embedded Systems (ViPES 2013) was a big success. The program started with the keynote "The Art of Virtual Prototyping -- Things we know how to do and things we do not know" from Dr. Christian Sauer (Cadence Design Systems, Munich, Germany), followed by seven very interesting talks from industry and academia. A best paper award was given to Tayeb Bouhadiba, Matthieu Moy, Florence Maraninchi, Jerome Cornet, Laurent Maillet-Contoz and Ilija Materic for their work on "Co-Simulation of Functional SystemC TLM Models with Power/Thermal Solvers". Additionally, the five best papers have been invited to a special issue of the ACM Transactions on Embedded Computing Systems (ACM TECS), which was organized for the workshop.

PhD Forum

PhD Forum is also another effort geared towards young researchers. It provides a platform to hone their presentation skills at poster presentation. At IPDPS PhD Forum is different in terms of attention given to individual students, each student get to interact with domain experts via mentoring sessions. And students have expressed their gratitude for such attention during recent conference. This year best poster presentation award is given to

- **Abhishek Gupta**, University of Illinois at Urbana-Champaign (Towards Efficient Mapping, Scheduling, and Execution of HPC Applications on Platforms in Cloud)
- **Ardavan Pedram**, University of Texas at Austin (Algorithm/Architecture Codesign of Low Power and High Performance Linear Algebra Compute Fabrics)
- **Thilo Schöndienst**, University of Massachusetts Dartmouth (Reducing the Environmental Impact of Optical Networks)

Upcoming TCPP sponsored and related meetings

Date	Event	Column1	Place
July 1, 2013	SNPD 2013 : 14th IEEE/ACIS International Conference on Software Engineering, Artificial Intelligence, Networking and Parallel/Distributed Computing		Honolulu, Hawaii, U.S.A.
	Acceptance notification	March 10, 2013	
	Camera-ready papers	March 30, 2013	
	Early Registration	April 5, 2013	
July 13, 2013	International Conference on Networking, Architecture, and Storage		Shaanxi, China
August 15, 2013	Extreme Scaling Workshop 2013		Boulder, Colorado
August 26, 2013	MVAPICH User Group (MUG)		Columbus, Ohio
	Submission Deadline	July 1, 2013	
	Notification of Acceptance	July 8, 2013	
September 7, 2013	22nd International Conference on Parallel Architectures and Compilation Techniques		Edinburgh, Scotland
	Abstracts	March 11, 2013	
	Papers	March 15, 2013	
	Rebuttal Period	April 28-30, 2013	
	Author Notification	May 20, 2013	
	Final Papers	TBD	
September 7, 2013	International workshop on "Data-Flow Models (DFM) for extreme scale computing		Edinburgh, Scotland
	Submission Deadline	July 15, 2013	
	Notification of Authors	August 10, 2013	
September 7, 2013	First Workshop on Parallel Quantum Computing (ParQ 2013)		Edinburgh, Scotland
	Submission Deadline	July 8, 2013	
	Author notification	July 19, 2013	
September 7, 2013	Tutorial on Graphite multicore simulator		Edinburgh, Scotland
September 7, 2013	Tutorial on SnuCL: An OpenCL Framework for Heterogeneous Clusters		Edinburgh, Scotland
September 10, 2013	Mini-Symposium on Parallel Computing using FPGAs		Munich, Germany
	Submission of paper	June 30, 2013	
	Notification of Acceptance	August 1, 2013	
	Submission of page for the book of abstracts	August 15, 2013	
	Submission of full paper	September 1, 2013	
September 10, 2013	6th International Workshop on Multi-/Many-core Computing Systems (MuCoCoS)		Munich, Germany
	Paper Submission	May 27, 2013	
	Notification	June 23, 2013	

Upcoming TCPP sponsored and related meetings (Contd.)

	Camera-ready papers for pre-workshop proceedings	July 7, 2013	
	Author registration	August 1, 2013	
	Camera-ready for post workshop proceeding	September 14, 2013	
October 1, 2013	ICPP-International Conference on Parallel Processing		Lyon, France
	Early Registration	August 25, 2013	
October 6, 2013	IEEE International Conference on Big Data		Silicon Valley, CA
	Full paper submission	June 23, 2013	
	Notification of Acceptance	August 10, 2013	
	Camera ready paper deadline	September 1, 2013	
October 23, 2013	International Symposium of Computer Architecture and Highperformance Computing		Brazil
	Submission Deadline	July 22, 2013	
	Author's notification	August 19, 2013	
	Camera ready	September 2, 2013	
October 30, 2013	IEEE/ACM DS-RT 2013		Delft, Netherlands
	Paper Submission	June 15, 2013	
	Posters/Demos submission	June 5, 2013	
	Notification of Acceptance	July 31, 2013	
	Camera Ready version	TBA	
	Early Registration	TBA	
	Speaker Author Registration	TBA	
November 17, 2013	Supercomputing Conference SC'13		Denver, Colorado
	Full Submissions due for Tutorial Proposals	March 25, 2013	
	Abstracts due for Technical Papers	April 5, 2013	
	Full Submissions due for Technical Papers	April 12, 2013	
	Full Submissions due for Student Cluster Competition	April 12, 2013	
	Full Submissions due for ACM Gordon Bell Prize	April 26, 2013	
	Full Submissions due for Panel Proposals	April 26, 2013	
	Abstracts due for SCinet Network Research Exhibition	May 2, 2013	
	Nominations due for ACM Gordon Bell Prize Submissions	May 3, 2013	
	Full Proposals due for SCinet Network Research Exhibition	May 31, 2013	
	Nominations due for IEEE Computer Society Sidney Fernbach Memorial Award Nominations	July 1, 2013	
	Nominations due for ACM - IEEE Computer Society Ken Kennedy Award Nominations	July 1, 2013	
	Nominations due for IEEE Computer Society Seymour Cray Computer Engineering Award Nominations	July 1, 2013	
	Full Submissions due for Birds-of-a-Feather Session Proposals	July 31, 2013	
	Full Submissions due for Posters	July 31, 2013	
	Full Submissions due for ACM Student Research Competition	July 31, 2013	
	Full Submissions due for Student Volunteers	July 31, 2013	
	Full Submissions due for Broader Engagement	July 31, 2013	

Upcoming TCPP sponsored and related meetings (Contd.)

	Full Submissions due for HPC Educators Program	July 31, 2013	
	Full Submissions due for Doctoral Showcase	July 31, 2013	
	Occupancy lists for exhibitor housing blocks due	September 1, 2013	
	General housing reservations closes	October 16, 2013	
	Off-site registration closes	November 14, 2013	
December 15, 2013	ICPADS		Seoul, Korea
	Paper submission	July 1, 2013	
	Notification of workshop / tutorial acceptance	July 8, 2013	
	Deadline for poster / demo submission	September 2, 2013	
	Notification of paper acceptance	September 2, 2013	
	Notification of poster / demo acceptance	September 9, 2013	
	Early Registration	November 4, 2013	
	Author registration	September 30, 2013	
	Camera-ready papers	October 7, 2013	
December 18, 2013	HiPC 2013		Hyderabad, India
	Workshop Proposals	March 15, 2013	
	Conference Paper	June 8, 2013	
	Notification of Acceptance	August 6, 2013	
	Camera-ready papers	August 25, 2013	
	Student Symposium Submission	September 16, 2013	
	Student Symposium Submission Decisions	October 21, 2013	
	Early Registration	November 14, 2013	
December 18, 2013	2nd Workshop on Performance Engineering and Applications (WPEA)		Hyderabad, India
	Paper Submission	July 8, 2013	
	Author Notification	August 12, 2013	
	Camera ready paper deadline	August 25, 2013	
December 18, 2013	Workshop on Parallel Algorithms and Software for Analysis of Massive Graphs (ParGraph)		Hyderabad, India
	Paper Submission	July 7, 2013	
	Author Notification	August 7, 2013	
	Camera ready paper deadline	August 15, 2013	
December 18, 2013	2nd International Workshop on Cloud Computing Applications (IWCA)		Hyderabad, India
	Paper Submission	July 15, 2013	
	Author Notification	August 15, 2013	
	Camera ready paper deadline	August 25, 2013	
December 18, 2013	Roadmapping Sandpit Workshop on Next-Generation Spatial Hardware Architectures		Hyderabad, India
	Paper Submission	August 1, 2013	
	Author Notification	August 12, 2013	
	Camera ready paper deadline	TBA	
December 18, 2013	6th HiPC Student Research Symposium (SRS)		Hyderabad, India
	Submission Opens	August 1, 2013	
	Submission Deadline	September 16, 2013	
	Accept/Reject Decisions	October 15, 2013	

Upcoming TCPP sponsored and related meetings (Contd.)

March 11, 2014	The IEEE International Conference on Cloud Engineering		Boston, Massachu-setts
	Abstract submission	September 14, 2013	
	Paper submission	September 21, 2013	
	Acceptance notification	December 13, 2013	
	Camera-ready papers	January 17, 2014	
	Author registration	January 17, 2013	
	Early Registration	February 7, 2014	

Technical Committee on Parallel Processing

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